TPS62748 300/400mA 高效降压转换器，具有超低静态电流和负载开关

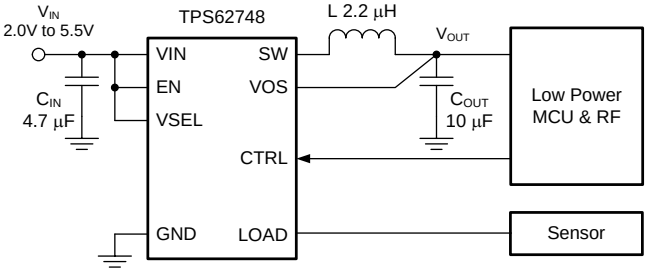
1 特性

- 2.15V 至 5.5V 的输入电压 (V_{IN}) 范围
- 启动后的输入电压范围低至 2.0V
- 300mA 输出电流
- 360nA 工作静态电流
- 10µA 输出电流时的效率高达 90%
- 节能模式操作
- 可选输出电压
  - 1.2V 和 1.8V
- 输出电压放电
- 100mA/2.2Ω 集成负载开关
- 低输出电压纹波
- 自动转换至无纹波 100% 模式
- 射频 (RF) 友好型 DCS-Control™
- 总体解决方案尺寸 < 10mm^2
- 小型 1.6mm × 0.9mm、8 焊球 WCSP 封装

2 应用

- 可穿戴产品
- 健身追踪器
- 智能手表
- 健康监测
- 蓝牙®低耗能、RF4CE、Zigbee
- 高效率、超低功耗 应用
- 能量采集

典型应用



3 说明

TPS62748 是一款高效降压转换器，具有典型值为 360nA 的超低静态电流。该器件经优化可搭配 2.2µH 电感和 10µF 输出电容正常工作。该器件采用 DCS-Control™ 技术，开关频率典型值为 1.2MHz。在节能模式下，该器件可将轻负载效率向下扩展至 10µA 负载电流及以下。TPS62748 提供 300mA 的输出电流。启动后，该器件可在低至 2.0V 的输入电压下工作。因此，可直接由单节 Li-MnO_2 纽扣电池为器件供电。

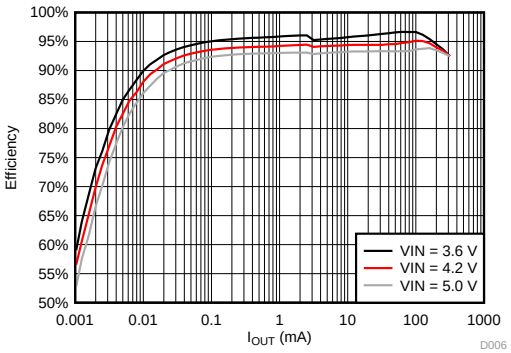
TPS62748 提供了两个可编程的输出电压（1.2V 和 1.8V），可通过一个电压选择引脚进行选择。 The TPS62748 经过优化，只需使用一个小型输出电容即可获得低输出电压纹波和低噪声。一旦输入电压接近输出电压，器件便会进入无纹波 100% 模式，以防止输出纹波电压增大。在此工作模式下，器件会停止开关操作并导通高侧MOSFET。

TPS62748 的 VOS 和 LOAD 引脚之间有一个 100mA 的负载开关，导通电阻典型值为 0.6Ω。该负载开关通常用于将降压转换器的输出电压分配给子系统。

器件信息(1)

器件型号	封装	封装尺寸（标称值）
TPS62748	DSBGA (8)	1.6mm × 0.9mm

(1) 要了解所有可用封装，请参阅数据表末尾的可订购产品附录。



An IMPORTANT NOTICE at the end of this data sheet addresses availability, warranty, changes, use in safety-critical applications, intellectual property matters and other important disclaimers. PRODUCTION DATA.

目录

1 特性 ... 1
2 应用 ... 1
3 说明 ... 1
4 修订历史记录 ... 2
5 **Device Comparison Table** ... 3
6 **Pin Configuration and Functions** ... 3
7 **Specifications** ... 4
7.1 Absolute Maximum Ratings ... 4
7.2 ESD Ratings ... 4
7.3 Recommended Operating Conditions ... 4
7.4 Thermal Information ... 5
7.5 Electrical Characteristics ... 5
7.6 Timing Requirements ... 6
7.7 Typical Characteristics ... 7
8 **Detailed Description** ... 8
8.1 Overview ... 8
8.2 Functional Block Diagram ... 8
8.3 Feature Description ... 8
8.4 Device Functional Modes ... 10
9 **Application and Implementation** ... 11
9.1 Application Information ... 11
9.2 Typical Application ... 11
9.3 System Example ... 17
10 **Power Supply Recommendations** ... 18
11 **Layout** ... 18
11.1 Layout Guidelines ... 18
11.2 Layout Example ... 18
12 器件和文档支持 ... 19
12.1 器件支持 ... 19
12.2 相关链接 ... 19
12.3 社区资源 ... 19
12.4 商标 ... 19
12.5 静电放电警告 ... 19
12.6 Glossary ... 19
13 机械、封装和可订购信息 ... 19

4 修订历史记录

Changes from Original (September 2015) to Revision A **Page**

- 已更改 将特性项目从“60nA 工作静态电流”更改成了“360nA 工作静态电流” ... 1

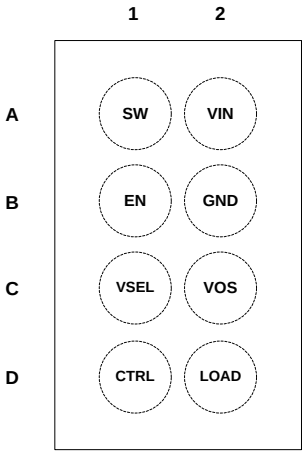
版权 © 2015, Texas Instruments Incorporated

5 Device Comparison Table

T_A	PART NUMBER	OUTPUT VOLTAGE SETTINGS (VSEL 1 - 3)	OUTPUT CURRENT	PACKAGE MARKING
–40°C to 85°C	TPS62748	1.2 V and 1.8 V (VSEL)	300 mA	TPS748

6 Pin Configuration and Functions

YFP Package
8-Pin DSBGA
Top View



Pin Functions TPS62748

PIN		I/O	DESCRIPTION
NAME	NO		
VIN	A2	PWR	V_{IN} power supply pin. Connect the input capacitor close to this pin for best noise and voltage spike suppression. A ceramic capacitor of 4.7 µF is required.
SW	A1	OUT	The switch pin is connected to the internal MOSFET switches. Connect the inductor to this terminal.
GND	B2	PWR	GND supply pin. Connect this pin close to the GND terminal of the input and output capacitor.
VOS	C2	IN	Feedback pin for the internal feedback divider network and regulation loop. An internal load switch is connected between VOUT pin and LOAD pin. Connect this pin directly to the output capacitor with a short trace. Discharges V_{OUT} when the converter is disabled.
VSEL	C1	IN	Output voltage selection pin. See Table 1 for V_{OUT} selection. This pin must be terminated. The pin can be dynamically changed during operation.
EN	B1	IN	High level enables the devices, low level turns the device into shutdown mode. The pin must be terminated.
CTRL	D1	IN	This pin controls the load switch between VOS and LOAD. With CTRL = low, the LOAD switch is disabled. This pin must be terminated.
LOAD	D2	OUT	Output terminal of the internal load switch. With CTRL = high, the internal load switch connects VOS to the LOAD pin. This pin is pulled to GND with the CTRL = low. If not used, leave the pin open.

Table 1. Output Voltage Setting TPS62748

Device	V_{OUT}	VSEL
TPS62748	1.2	0
	1.8	1

Copyright © 2015, Texas Instruments Incorporated

7 Specifications

7.1 Absolute Maximum Ratings

over operating free-air temperature range (unless otherwise noted) (1)

		MIN	MAX	UNIT
Pin voltage(2)	VIN	–0.3	6	V
	SW, VSEL, LOAD, CTRL	–0.3	V_{IN} +0.3V	V
	EN	–0.3	V_{IN} +0.3V	V
	VOS	–0.3	3.7	V
Operating junction temperature, T_J		–40	125	°C
Storage temperature, T_{stg}		–65	150	°C

(1) Stresses beyond those listed under *absolute maximum ratings* may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other conditions beyond those indicated under *recommended operating conditions* is not implied. Exposure to absolute–maximum–rated conditions for extended periods may affect device reliability.
(2) All voltage values are with respect to network ground terminal GND.

7.2 ESD Ratings

			VALUE	UNIT
$V_{(ESD)}$	Electrostatic discharge	Human body model (HBM), per ANSI/ESDA/JEDEC JS-001, all pins(1)	±2000	V
		Charged device model (CDM), per JEDEC specification JESD22-C101, all pins(2)	±500	

(1) JEDEC document JEP155 states that 500-V HBM allows safe manufacturing with a standard ESD control process. The human body model is a 100-pF capacitor discharged through a 1.5-kΩ resistor into each pin.
(2) JEDEC document JEP157 states that 250-V CDM allows safe manufacturing with a standard ESD control process.

7.3 Recommended Operating Conditions

			MIN	NOM	MAX	UNIT
V_{IN}	Supply voltage V_{IN}		2.15		5.5	V
V_{IN}	Supply voltage V_{IN} , once started		2.0		5.5	V
I_{OUT}	Device output current	TPS62743 / TPS627431 5.5V ≥ VIN ≥ (VOUTnom + 0.7V) ≥ 2.15V			300	mA
		5.5V ≥ VIN ≥ (VOUTnom + 0.7V) ≥ 3V			400	
I_{LOAD}	Load switch current				100	mA
T_J	Operating junction temperature range		-40		125	°C

Copyright © 2015, Texas Instruments Incorporated

7.4 Thermal Information

THERMAL METRIC(1)		TPS62748 YFP 8 PINS	UNIT
$R_{\theta JA}$	Junction-to-ambient thermal resistance	103	°C/W
$R_{\theta JCtop}$	Junction-to-case (top) thermal resistance	1.0	°C/W
$R_{\theta JB}$	Junction-to-board thermal resistance	20	°C/W
ψ_{JT}	Junction-to-top characterization parameter	0.3	°C/W
ψ_{JB}	Junction-to-board characterization parameter	20	°C/W
$R_{\theta JCbot}$	Junction-to-case (bottom) thermal resistance	N/A	°C/W

(1) For more information about traditional and new thermal metrics, see the *Semiconductor and IC Package Thermal Metrics* application report, SPRA953.

7.5 Electrical Characteristics

V_{IN} = 3.6V, T_A = –40°C to 85°C typical values are at T_A = 25°C (unless otherwise noted)

PARAMETER		TEST CONDITIONS	MIN	TYP	MAX	UNIT
SUPPLY						
I_Q	Operating quiescent current	EN = V_{IN}, CTRL = GND, I_{OUT} = 0µA, V_{OUT} = 1.8V, device not switching		360	1800	nA
		EN = V_{IN}, I_{OUT} = 0mA, CTRL = GND, V_{OUT} = 1.8V , device switching		460		
I_{SD}	Shutdown current	EN = GND, shutdown current into V_{IN}		70	1000	nA
V_{TH_UVLO+}	Undervoltage lockout threshold	Rising V_{IN}		2.075	2.15	V
V_{TH_UVLO-}		Falling V_{IN}		1.925	2	
INPUTS (EN, VSEL, CTRL)						
$V_{IH\ TH}$	High level input threshold	2.2V ≤ V_{IN} ≤ 5.5V			1.1	V
$V_{IL\ TH}$	Low level input threshold	2.2V ≤ V_{IN} ≤ 5.5V	0.4			V
I_{IN}	Input bias Current			10	25	nA
POWER SWITCHES						
$R_{DS(ON)}$	High side MOSFET on-resistance	I_{OUT} = 50mA		0.45	1.12	Ω
	Low Side MOSFET on-resistance			0.22	0.65	
I_{LIMF}	High side MOSFET switch current limit		480	600	720	mA
			590	650	800	
	Low side MOSFET switch current limit	TPS62743		600		
		TPS627431		650		
OUTPUT VOLTAGE DISCHARGE						
R_{DSCH_VOS}	MOSFET on-resistance	EN = GND, I_{VOS} = -10mA into VOS pin		30	65	Ω
I_{IN_VOS}	Bias current into VOS pin	EN = V_{IN}, V_{OUT} = 2V		40	1010	nA
LOAD Switch						
R_{LOAD}	MOSFET on-resistance	I_{LOAD} = 50mA, CTRL = V_{IN}, V_{OUT} = 1.8V, 2.2V ≤ V_{IN} ≤ 5.5V		0.6	1.27	Ω
R_{DCHRG}	MOSFET on-resistance	2.2V ≤ V_{IN} ≤ 5.5V; CTRL=GND		30	65	Ω

Copyright © 2015, Texas Instruments Incorporated

Electrical Characteristics (continued)

V_{IN} = 3.6V, T_A = –40°C to 85°C typical values are at T_A = 25°C (unless otherwise noted)

PARAMETER		TEST CONDITIONS	MIN	TYP	MAX	UNIT
AUTO 100% MODE TRANSITION						
V_{TH_100+}	Auto 100% Mode leave detection threshold (1)	Rising V_{IN},100% Mode is left with $V_{IN} = V_{OUT} + V_{TH_100+}$	150	250	350	mV
V_{TH_100-}	Auto 100% Mode enter detection threshold (1)	Falling V_{IN}, 100% Mode is entered with $V_{IN} = V_{OUT} + V_{TH_100-}$	85	200	290	mV
OUTPUT						
$I_{LIM_softstart}$	High side softstart switch current limit	EN=low to high	80	150	200	mA
	Low side softstart switch current limit			150		mA
V_{OUT}	Output voltage range	Output voltages are selected with pins VSEL	1.2		1.8	V
	Output voltage accuracy	I_{OUT} = 10mA, V_{OUT} = 1.8V	-2.5	0%	2.5	V
		I_{OUT} = 100mA, V_{OUT} = 1.8V	–2	0%	2	V
	DC output voltage load regulation	V_{OUT} = 1.8V		0.001		%/mA
	DC output voltage line regulation	V_{OUT} = 1.8V, I_{OUT} = 100mA, 2.2V ≤ V_{IN} ≤ 5.0V		0		%/V

(1) V_{IN} is compared to the programmed output voltage (V_{OUT}). When V_{IN}–V_{OUT} falls below V_{TH_100-} the device enters 100% Mode by turning the high side MOSFET on. The 100% Mode is exited when V_{IN}–V_{OUT} exceeds V_{TH_100+} and the device starts switching. The hysteresis for the 100% Mode detection threshold V_{TH_100+} - V_{TH_100-} will always be positive and will be approximately 50 mV(typ)

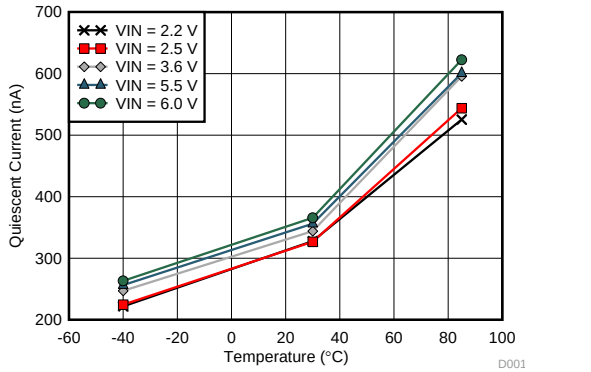
7.6 Timing Requirements

V_{IN} = 3.6V, T_J = –40°C to 85°C typical values are at T_A = 25°C (unless otherwise noted)

PARAMETER		TEST CONDITIONS	MIN	TYP	MAX	UNIT
LOAD Switch						
t_{rise_LOAD}	V_{LOAD} rise time	CTRL= low to high, V_{OUT} = 1.8V, 2.2V ≤ V_{IN} ≤ 5.5V, I_{LOAD} = 20mA		315	800	µs
OUTPUT						
t_{ONmin}	Minimum ON time	V_{OUT} = 2.0V, I_{OUT} = 0 mA		225		ns
t_{OFFmin}	Minimum OFF time	V_{IN} = 2.3V		50		ns
$t_{Startup_delay}$	Regulator start up delay time	From transition EN = low to high until device starts switching		10	25	ms
$t_{Softstart}$	Softstart time	2.2V ≤ V_{IN} ≤ 5.5V, EN = V_{IN}		700	1200	µs

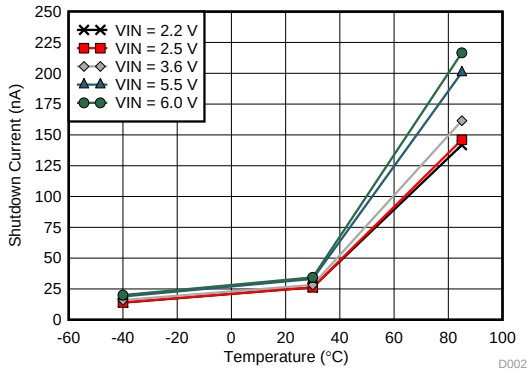
版权 © 2015, Texas Instruments Incorporated

7.7 Typical Characteristics



EN = VIN, V_{OUT} = 1.8V Device Not Switching

图 1. Quiescent Current vs Temperature



EN = GND

图 2. Shutdown Current I_{SD} vs Temperature

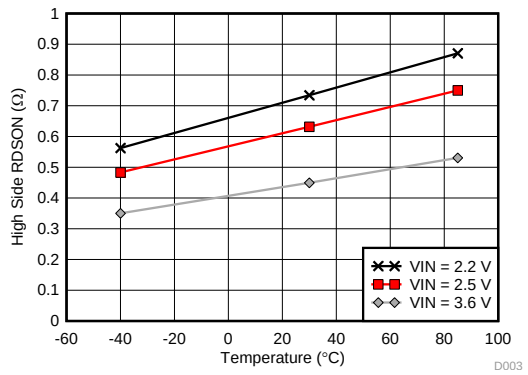


图 3. High Side R_{DSON} vs Temperature

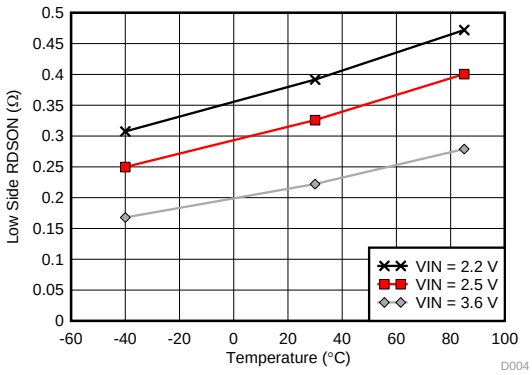


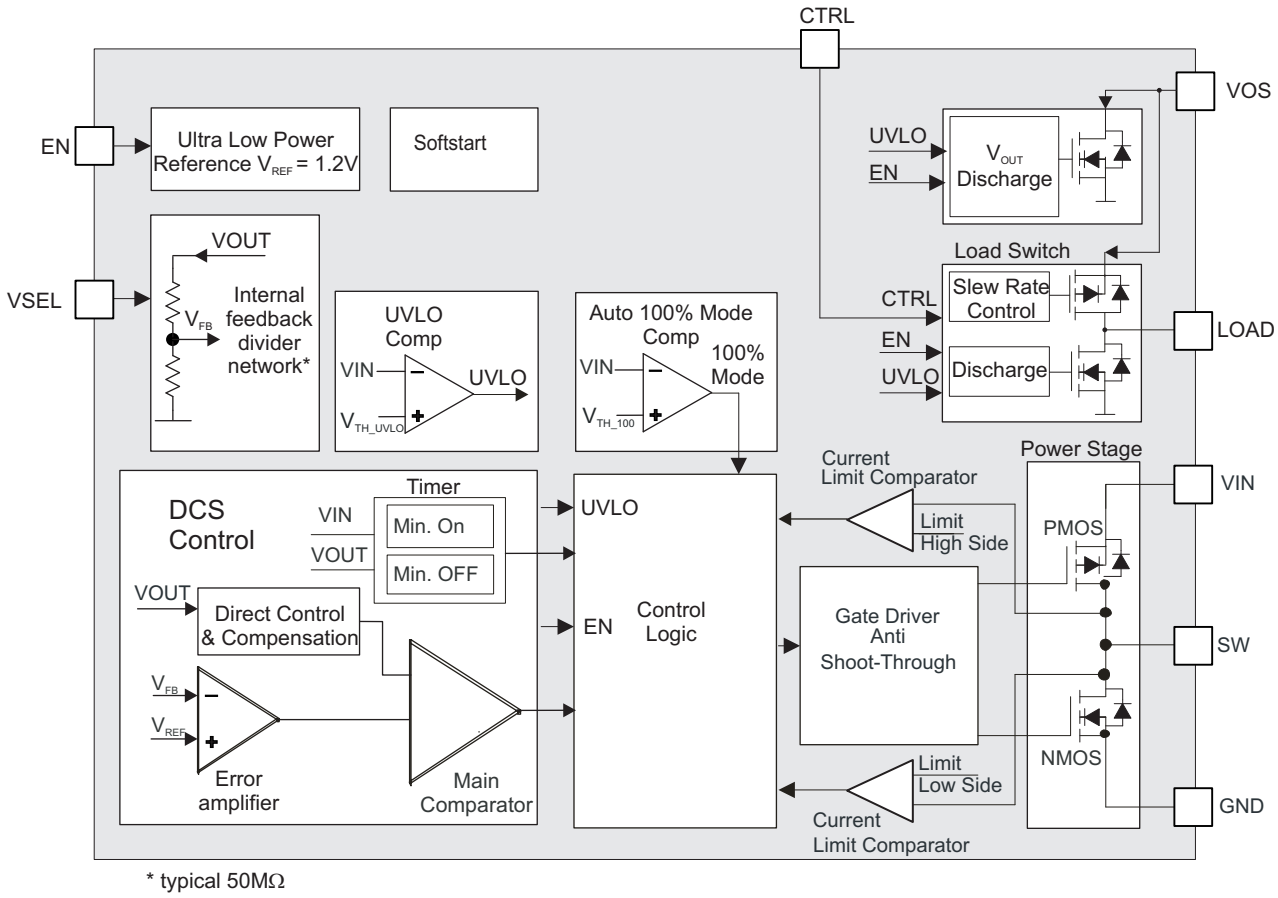
图 4. Low-side R_{DSON} vs Temperature

8 Detailed Description

8.1 Overview

The TPS62748 is a high frequency step down converter with ultra low quiescent current. The device operates with a quasi fixed switching frequency typically at 1.2 MHz. Using TI's DCS-Control™ topology the device extends the high efficiency operation area down to a few microamperes of load current during Power Save Mode Operation.

8.2 Functional Block Diagram



* typical 50MΩ

8.3 Feature Description

8.3.1 DCS-Control™

TI's DCS-Control™ (Direct Control with Seamless Transition into Power Save Mode) is an advanced regulation topology, which combines the advantages of hysteretic and voltage mode control. Characteristics of DCS-Control™ are excellent AC load regulation and transient response, low output ripple voltage and a seamless transition between PFM and PWM mode operation. DCS-Control™ includes an AC loop which senses the output voltage (VOS pin) and directly feeds the information to a fast comparator stage. This comparator sets the switching frequency, which is constant for steady state operating conditions, and provides immediate response to dynamic load changes. In order to achieve accurate DC load regulation, a voltage feedback loop is used. The internally compensated regulation network achieves fast and stable operation with small external components and low ESR capacitors.

版权 © 2015, Texas Instruments Incorporated

Feature Description (接下页)

The DCS-Control™ topology supports PWM (Pulse Width Modulation) mode for medium and high load conditions and a Power Save Mode at light loads. During PWM mode, it operates in continuous conduction mode. The switching frequency is typically 1.2 MHz with a controlled frequency variation depending on the input voltage and load current. If the load current decreases, the converter seamlessly enters Power Save Mode to maintain high efficiency down to very light loads. In Power Save Mode, the switching frequency varies linearly with the load current. Since DCS-Control™ supports both operation modes within one single building block, the transition from PWM to Power Save Mode is seamless with minimum output voltage ripple. The TPS62748 offers both excellent DC voltage and superior load transient regulation, combined with low output voltage ripple, minimizing interference with RF circuits.

8.3.2 Power Save Mode Operation

In Power Save Mode the device operates in PFM (Pulse Frequency Modulation) that generates a single switching pulse to ramp up the inductor current and recharges the output capacitor, followed by a sleep period where most of the internal circuits are shutdown to achieve lowest operating quiescent current. During this time, the load current is supported by the output capacitor. The duration of the sleep period depends on the load current and the inductor peak current. During the sleep periods, the current consumption of TPS62748 is reduced to 360 nA. This low quiescent current consumption is achieved by an ultra low power voltage reference, an integrated high impedance feedback divider network and an optimized Power Save Mode operation.

8.3.3 100mA Load Switch

The load switch connects VOS pin to the LOAD pin and is able to support 100 mA. The on-resistance depends on the output voltage. At 1.2 V Vout the on-resistance is maximum 2.2 Ω and at 1.8 V Vout the on-resistance is maximum 1.2Ω. The load switch can be used to power a sub system. With CTRL=high the load switch is turned on. To avoid a voltage drop at the output of the buck converter the load switch has a internal softstart. With CTRL=low the load switch is turned off and the LOAD pin is internally discharged to GND by typically 30Ω. This makes sure the output of the load switch is always discharged to GND before the load switch is turned on again.

8.3.4 Output Voltage Selection

The TPS62748 doesn't require an external resistor divider network to program the output voltage. The device integrates a high impedance feedback resistor divider network which is programmed by the pin VSEL. TPS62748 supports two output voltage options: 1.2 V and 1.8 V. The output voltage is programmed according to Table 1. The output voltage can be changed during operation and supports a simple dynamic output voltage scaling.

8.3.5 Output Voltage Discharge of the Buck Converter

The device provides automatic output voltage discharge when EN is pulled low or the UVLO is triggered. The output of the buck converter is discharged over VOS. Because of this the output voltage will ramp up from zero once the device is enabled again. This is very helpful for accurate start-up sequencing.

8.3.6 Undervoltage Lockout UVLO

To avoid misoperation of the device at low input voltages, an undervoltage lockout is used. The UVLO shuts down the device at a maximum voltage level of 2.0 V. The device will start at a UVLO level of 2.15 V.

8.3.7 Short circuit protection

The TPS6274x integrates a current limit on the high side, as well on the low side MOSFETs to protect the device against overload or short circuit conditions. The peak current in the switches is monitored cycle by cycle. If the high side MOSFET current limit is reached, the high side MOSFET is turned off and the low side MOSFET is turned on until the switch current decreases below the low side MOSFET current limit. Once the low side MOSFET current limit trips, the low side MOSFET is turned off and the high side MOSFET turns on again.

版权 © 2015, Texas Instruments Incorporated

8.4 Device Functional Modes

8.4.1 Enable and Shutdown

The device is turned on with EN=high. With EN=low the device enters shutdown and turns off the load switch. This pin must be terminated.

8.4.2 Device Start-up and Softstart

The device has an internal softstart to minimize input voltage drop during start-up. This allows the operation from high impedance battery cells. Once the device is enabled the device starts switching after a typical delay time of 10ms. Then the softstart time of typical 700 µs begins with a reduced current limit of typical 150 mA. When this time passed by the device enters full current limit operation. This allows a smooth start-up and the device can start into full load current. Furthermore, larger output capacitors impact the start-up behaviour of the DC/DC converter. Especially when the output voltage does not reach its nominal value after the typical soft-start time of 700 µs, has passed.

8.4.3 Automatic Transition Into No Ripple 100% Mode

Once the input voltage comes close to the output voltage, the DC/DC converter stops switching and enters 100% duty cycle operation. It connects the output V_{OUT} via the inductor and the internal high side MOSFET switch to the input VIN, once the input voltage V_{IN} falls below the 100% mode enter threshold, V_{TH_100-}. The DC/DC regulator is turned off, switching stops and therefore no output voltage ripple is generated. Since the output is connected to the input, the output voltage follows the input voltage minus the voltage drop across the internal high side switch and the inductor. Once the input voltage increases and trips the 100% mode exit threshold, V_{TH_100+} , the DC/DC regulator turns on and starts switching again. See 图 5 and 图 25.

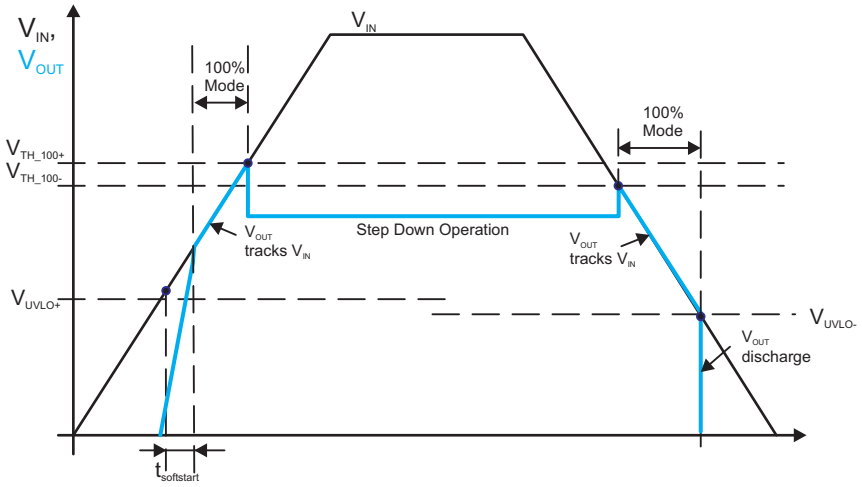


图 5. Automatic Transition into 100% Mode

版权 © 2015, Texas Instruments Incorporated

9 Application and Implementation

注

Information in the following applications sections is not part of the TI component specification, and TI does not warrant its accuracy or completeness. TI's customers are responsible for determining suitability of components for their purposes. Customers should validate and test their design implementation to confirm system functionality.

9.1 Application Information

The TPS62748 is a high efficiency step down converter with ultra low quiescent current of typically 360 nA. The device operates with a tiny 2.2-µH inductor and 10-µF output capacitor over the entire recommended operation range. A dedicated measurement set-up is required for the light load efficiency measurement and device quiescent current due to the operation in the sub microampere range. In this range any leakage current in the measurement set-up will impact the measurement results.

9.2 Typical Application

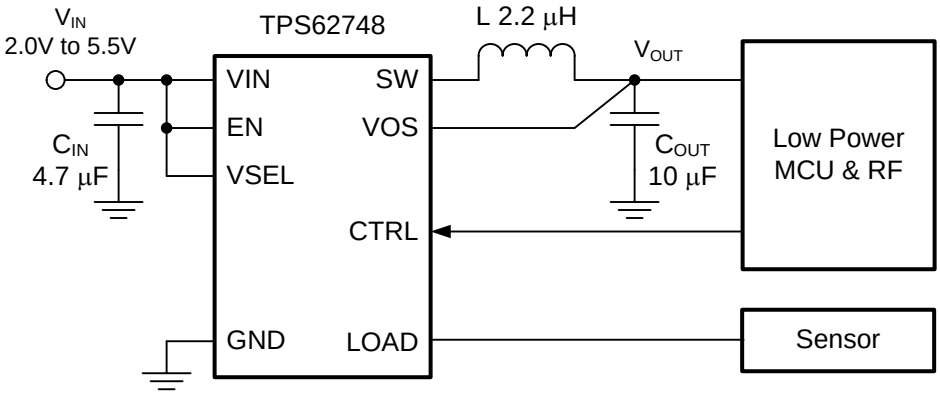


图 6. TPS62748 Typical Application Circuit

9.2.1 Design Requirements

The TPS62748 is a highly integrated DC/DC converter. The output voltage is set via a VSEL pin interface. The design guideline provides a component selection to operate the device within the recommended operating conditions.

表 2 shows the list of components for the Application Characteristic Curves.

Typical Application (接下页)

表 2. Components for Application Characteristic Curves

Reference	Description	Value	Manufacturer
TPS62748	360nA Iq step down converter		Texas Instruments
CIN	Ceramic capacitor, GRM155R61C475ME15	4.7 µF	Murata
COUT	Ceramic capacitor, GRM155R60J106ME11	10 µF	Murata
L	Inductor DFE201610C	2.2 µH	Toko

9.2.2 Detailed Design Procedure

The first step in the design procedure is the selection of the output filter components. To simplify this process, 表 3 outlines possible inductor and capacitor value combinations.

表 3. Recommended LC Output Filter Combinations

Inductor Value [µH](1)	Output Capacitor Value [µF](2)				
	4.7µF	10µF	22µF	47µF	100µF
2.2	√	√(3)	√	√	

(1) Inductor tolerance and current de-rating is anticipated. The effective inductance can vary by 20% and -30%.
(2) Capacitance tolerance and bias voltage de-rating is anticipated. The effective capacitance varies by +20% and –50%.
(3) Typical application configuration. Other check marks indicate alternative filter combinations.

9.2.2.1 Inductor Selection

The inductor value affects the peak-to-peak ripple current, the PWM-to-PFM transition point, the output voltage ripple and the efficiency. The selected inductor has to be rated for its DC resistance and saturation current. The inductor ripple current (ΔI_L) decreases with higher inductance and increases with higher V_{IN} or V_{OUT} and can be estimated according to 公式 1.

公式 2 calculates the maximum inductor current under static load conditions. The saturation current of the inductor should be rated higher than the maximum inductor current, as calculated with 公式 2. This is recommended because during a heavy load transient the inductor current rises above the calculated value. A more conservative way is to select the inductor saturation current according to the high-side MOSFET switch current limit, I_{LIMF}.

$$\Delta I_L = Vout \times \frac{1 - \frac{Vout}{Vin}}{L \times f} \quad (1)$$

$$I_{Lmax} = I_{outmax} + \frac{\Delta I_L}{2} \quad (2)$$

where

- f = Switching Frequency
- L = Inductor Value
- ΔI_L = Peak to Peak inductor ripple current
- I_{Lmax} = Maximum Inductor current

表 4 shows a list of possible inductors.

版权 © 2015, Texas Instruments Incorporated

表 4. List of Possible Inductors(1)

INDUCTANCE [µH]	DIMENSIONS [mm³]	INDUCTOR TYPE	Isat/DCR	SUPPLIER	Comment
2.2	2.0 x 1.6 x 1.0	DFE201610C	1.4 A/170 mΩ	TOKO	Efficiency plot 图 13
2.2	2.0 × 1.25 × 1.0	MIPSZ2012D 2R2	0.7 A/230 mΩ	FDK	
2.2	2.0 x 1.2 x 1.0	744 797 752 22	0.7 A/200 mΩ	Würth Elektronik	
2.2	1.6 x 0.8 x 0.8	MDT1608-CH2R2M	0.7 A/300 mΩ	TOKO	

(1) See *Third-party Products Disclaimer*

9.2.2.2 Output Capacitor Selection

The DCS-Control™ scheme of the TPS62748 allows the use of tiny ceramic capacitors. Ceramic capacitors with low ESR values have the lowest output voltage ripple and are recommended. The output capacitor requires either an X7R or X5R dielectric. At light load currents, the converter operates in Power Save Mode and the output voltage ripple is dependent on the output capacitor value. A larger output capacitors can be used reducing the output voltage ripple. The leakage current of the output capacitor adds to the overall quiescent current.

9.2.2.3 Input Capacitor Selection

Because the buck converter has a pulsating input current, a low ESR input capacitor is required for best input voltage filtering to minimize input voltage spikes. For most applications a 4.7-µF input capacitor is sufficient. When operating from a high impedance source, like a coin cell a larger input buffer capacitor ≥10uF is recommended avoiding voltage drops during start-up and load transients. The input capacitor can be increased without any limit for better input voltage filtering. The leakage current of the input capacitor adds to the overall quiescent current. 表 5 shows a selection of input and output capacitors.

表 5. List of Possible Capacitors(1)

CAPACITANCE [µF]	SIZE	CAPACITOR TYPE	SUPPLIER
4.7	0402	GRM155R61C475ME15	Murata
10	0402	GRM155R60J106ME11	Murata

(1) See *Third-party Products Disclaimer*

版权 © 2015, Texas Instruments Incorporated

9.2.3 Application Curves

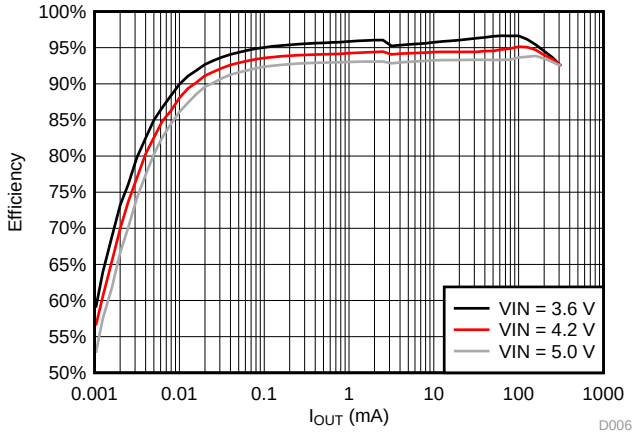


图 7. Efficiency vs Load Current, V_{OUT} = 3.3 V

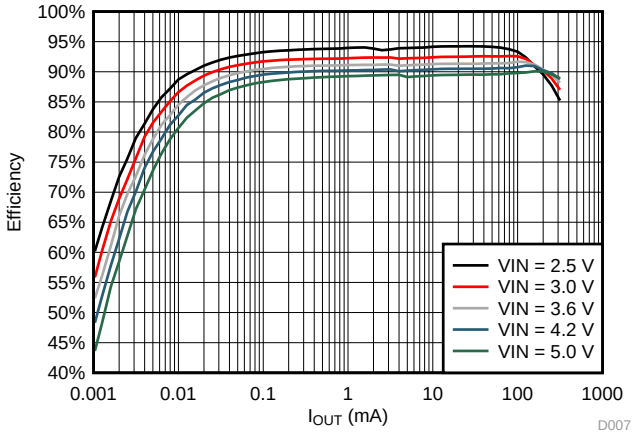


图 8. Efficiency vs Load Current; V_{OUT} = 2.1 V

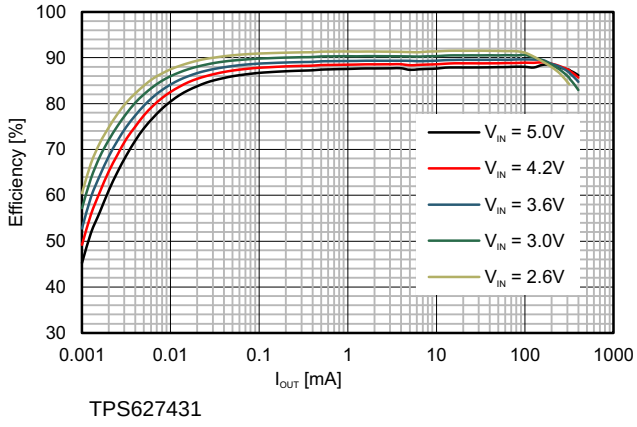


图 9. Efficiency vs Load Current; V_{OUT} = 1.9 V

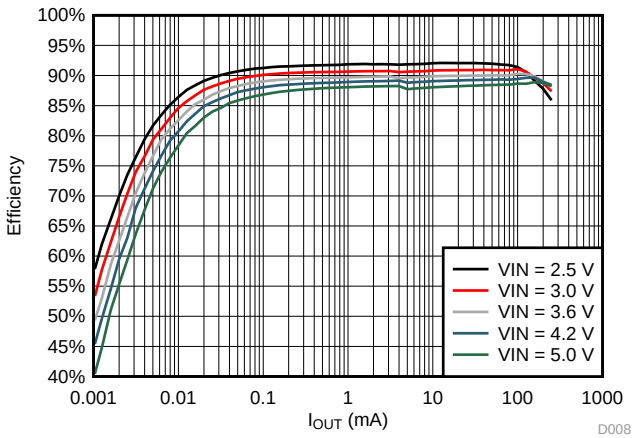


图 10. Efficiency vs Load Current; VOUT = 1.8 V

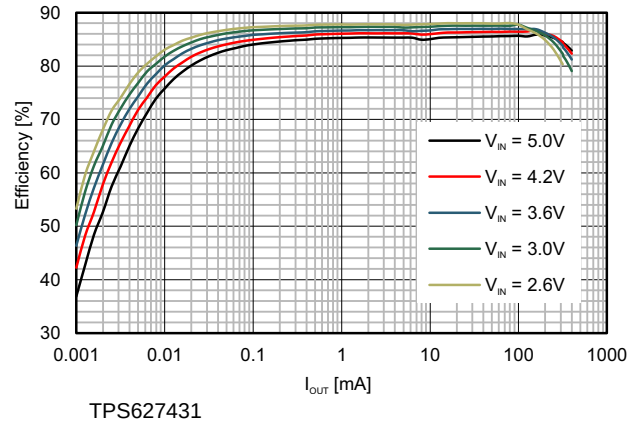


图 11. Efficiency vs Load Current; VOUT = 1.4 V

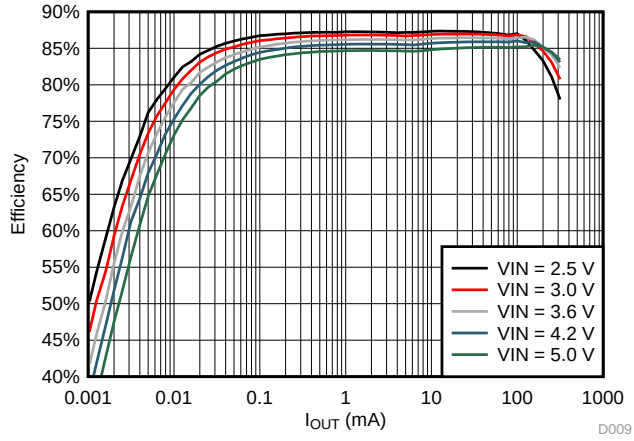


图 12. Efficiency vs Load Current; V_{OUT} = 1.2 V

版权 © 2015, Texas Instruments Incorporated

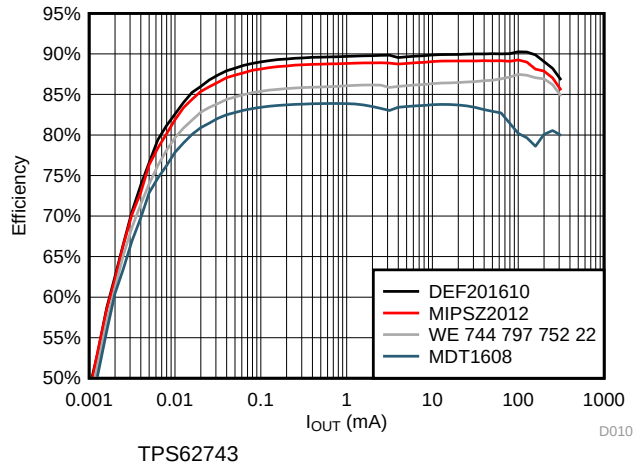


图 13. Efficiency vs Load Current; V_{OUT} = 1.8 V

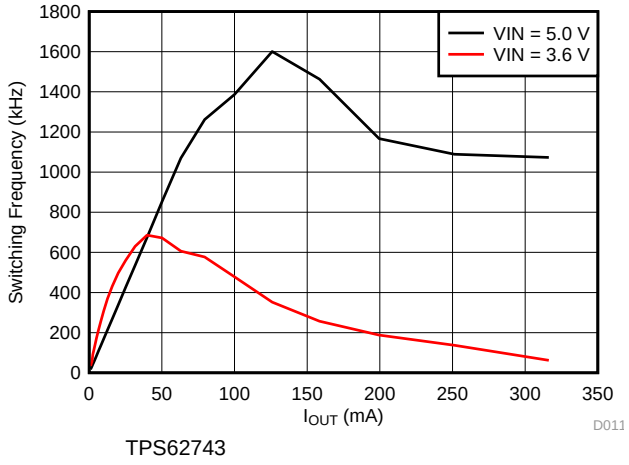


图 14. Switching Frequency vs Load Current V_{OUT} = 3.3 V

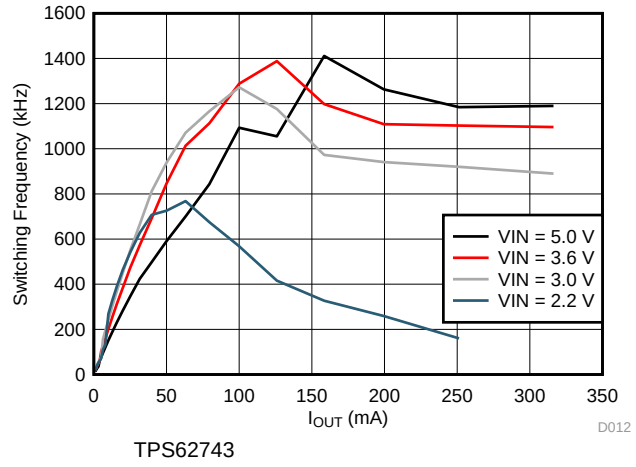


图 15. Switching Frequency vs Load Current V_{OUT} = 1.8 V

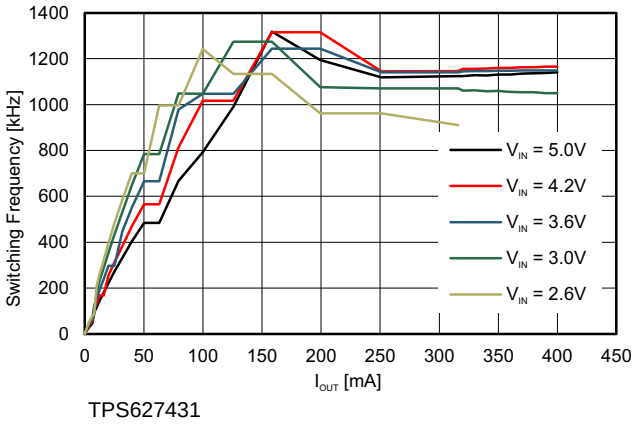


图 16. Switching Frequency vs Load Current V_{OUT} = 1.4 V

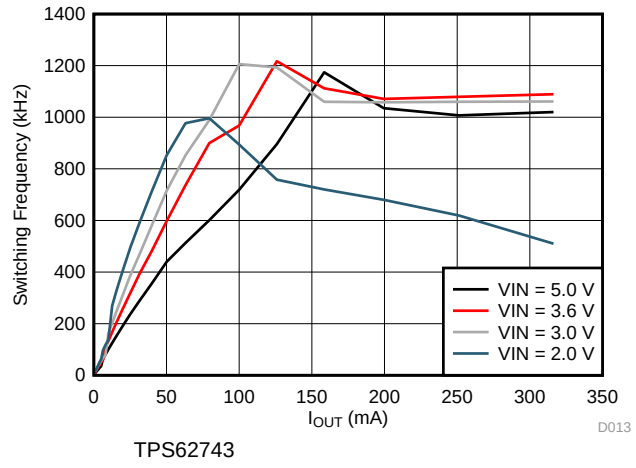


图 17. Switching Frequency vs Load Current V_{OUT} = 1.2 V

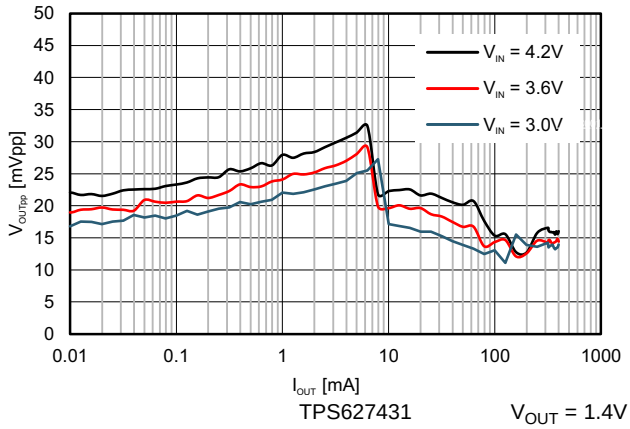


图 18. Typical Output Ripple Voltage VOUT = 1.4V

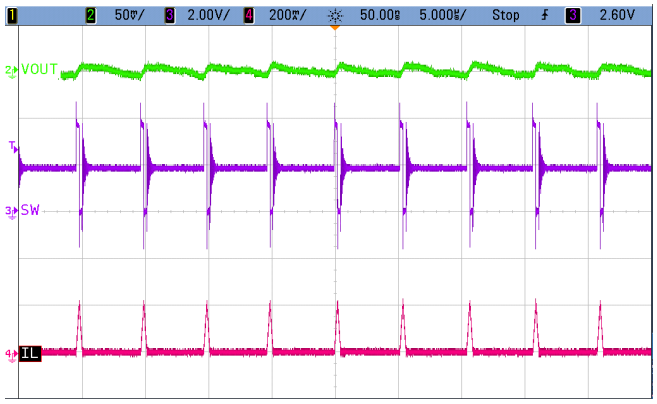


图 19. PFM (Power Save Mode) Mode Operation

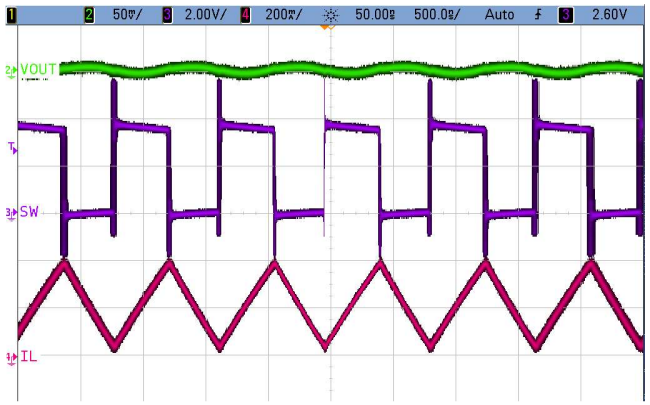


图 20. PWM Mode Operation

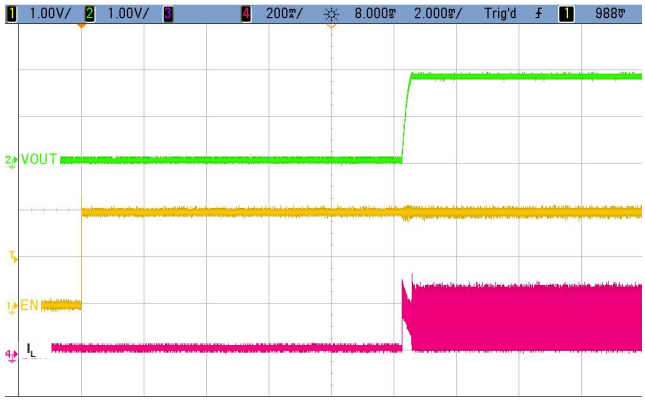


图 21. Startup Into 100 mA Electronic Load
EN Delay + Soft-Start Delay

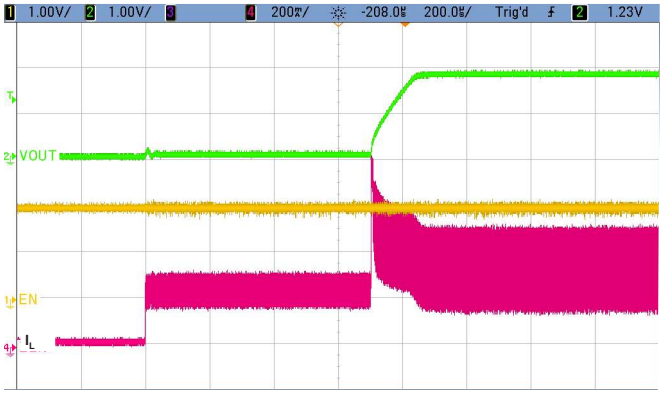


图 22. Startup Into 300 mA Electronic Load
Soft-Start Delay

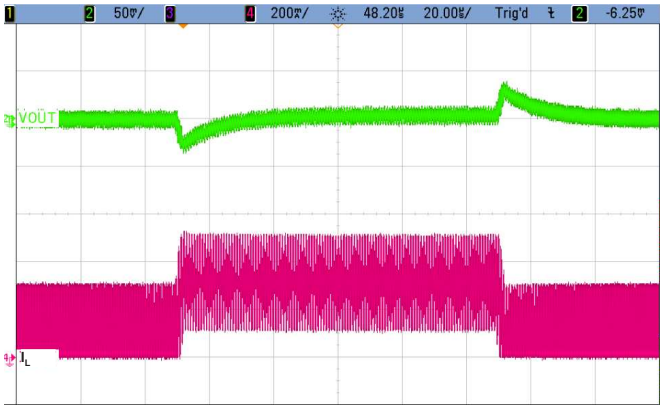


图 23. Load Transient Response; 100 mA to 290 mA

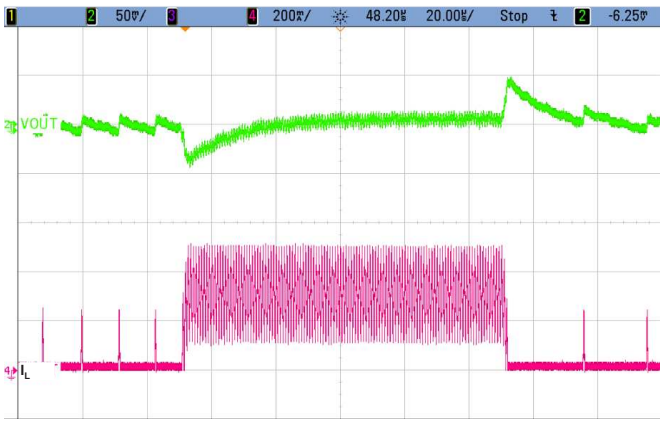


图 24. Load Transient Response; 5 mA to 290 mA

版权 © 2015, Texas Instruments Incorporated

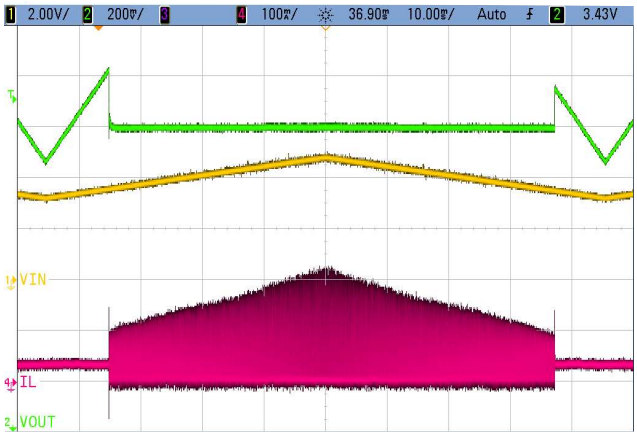


图 25. 100% Mode Entry and Leave Operation
I_{OUT} = 30 mA

9.3 System Example

10 Power Supply Recommendations

The power supply must provide a current rating according to the supply voltage, output voltage and output current of the TPS62748.

11 Layout

11.1 Layout Guidelines

- As for all switching power supplies, the layout is an important step in the design. Care must be taken in board layout to get the specified performance.
- It is critical to provide a low inductance, impedance ground path. Therefore, use wide and short traces for the main current paths.
- The input capacitor should be placed as close as possible to the IC pins VIN and GND. This is the most critical component placement.
- The V_{OS} line is a sensitive high impedance line and should be connected to the output capacitor and routed away from noisy components and traces (e.g. SW line) or other noise sources.

11.2 Layout Example

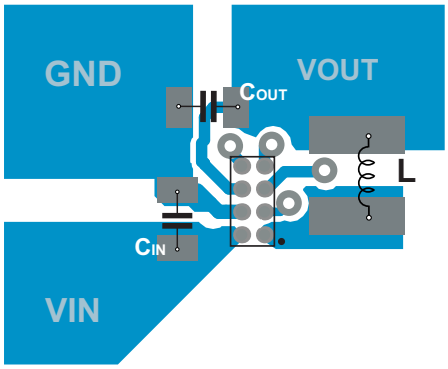


图 26. Recommended PCB Layout

版权 © 2015, Texas Instruments Incorporated

12 器件和文档支持

12.1 器件支持

12.1.1 Third-Party Products Disclaimer

TI'S PUBLICATION OF INFORMATION REGARDING THIRD-PARTY PRODUCTS OR SERVICES DOES NOT CONSTITUTE AN ENDORSEMENT REGARDING THE SUITABILITY OF SUCH PRODUCTS OR SERVICES OR A WARRANTY, REPRESENTATION OR ENDORSEMENT OF SUCH PRODUCTS OR SERVICES, EITHER ALONE OR IN COMBINATION WITH ANY TI PRODUCT OR SERVICE.

12.2 相关链接

下面的表格列出了快速访问链接。类别包括技术文档、支持与社区资源、工具和软件，以及申请样片或购买产品的快速链接。

12.3 社区资源

下列链接提供到 TI 社区资源的连接。链接的内容由各个分销商“按照原样”提供。这些内容并不构成 TI 技术规范，并且不一定反映 TI 的观点；请参阅 TI 的 《使用条款》。

TI E2E™ 在线社区 ***TI 的工程师对工程师 (E2E) 社区。*** **此社区的创建目的在于促进工程师之间的协作。在 e2e.ti.com 中，您可以咨询问题、分享知识、拓展思路并与同行工程师一道帮助解决问题。**

设计支持 ***TI 参考设计支持*** **可帮助您快速查找有帮助的 E2E 论坛、设计支持工具以及技术支持的联系信息。**

12.4 商标

DCS-Control, E2E are trademarks of Texas Instruments.
蓝牙 is a registered trademark of Bluetooth SIG, Inc.
All other trademarks are the property of their respective owners.

12.5 静电放电警告



这些装置包含有限的内置 ESD 保护。 存储或装卸时，应将导线一起截短或将装置放置于导电泡棉中，以防止 MOS 门极遭受静电损伤。

12.6 Glossary

SLYZ022 — *TI Glossary*.

This glossary lists and explains terms, acronyms, and definitions.

13 机械、封装和可订购信息

以下页面包含机械、封装和可订购信息。这些信息是指定器件的最新可用数据。这些数据如有变更，恕不另行通知和修订此文档。如欲获取此数据表的浏览器版本，请参阅左侧的导航。

版权 © 2015, Texas Instruments Incorporated

PACKAGING INFORMATION

Orderable Device	Status (1)	Package Type	Package Drawing	Pins	Package Qty	Eco Plan (2)	Lead/Ball Finish (6)	MSL Peak Temp (3)	Op Temp (°C)	Device Marking (4/5)	Samples
TPS62748YFPR	ACTIVE	DSBGA	YFP	8	3000	Green (RoHS & no Sb/Br)	SNAGCU	Level-1-260C-UNLIM	-40 to 125	TPS748	Samples
TPS62748YFPT	ACTIVE	DSBGA	YFP	8	250	Green (RoHS & no Sb/Br)	SNAGCU	Level-1-260C-UNLIM	-40 to 125	TPS748	Samples

(1) The marketing status values are defined as follows:
ACTIVE: Product device recommended for new designs.
LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.
NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.
PREVIEW: Device has been announced but is not in production. Samples may or may not be available.
OBSOLETE: TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details.
TBD: The Pb-Free/Green conversion plan has not been defined.
Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.
Pb-Free (RoHS Exempt): This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.
Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

(3) MSL, Peak Temp. - The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

(4) There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.

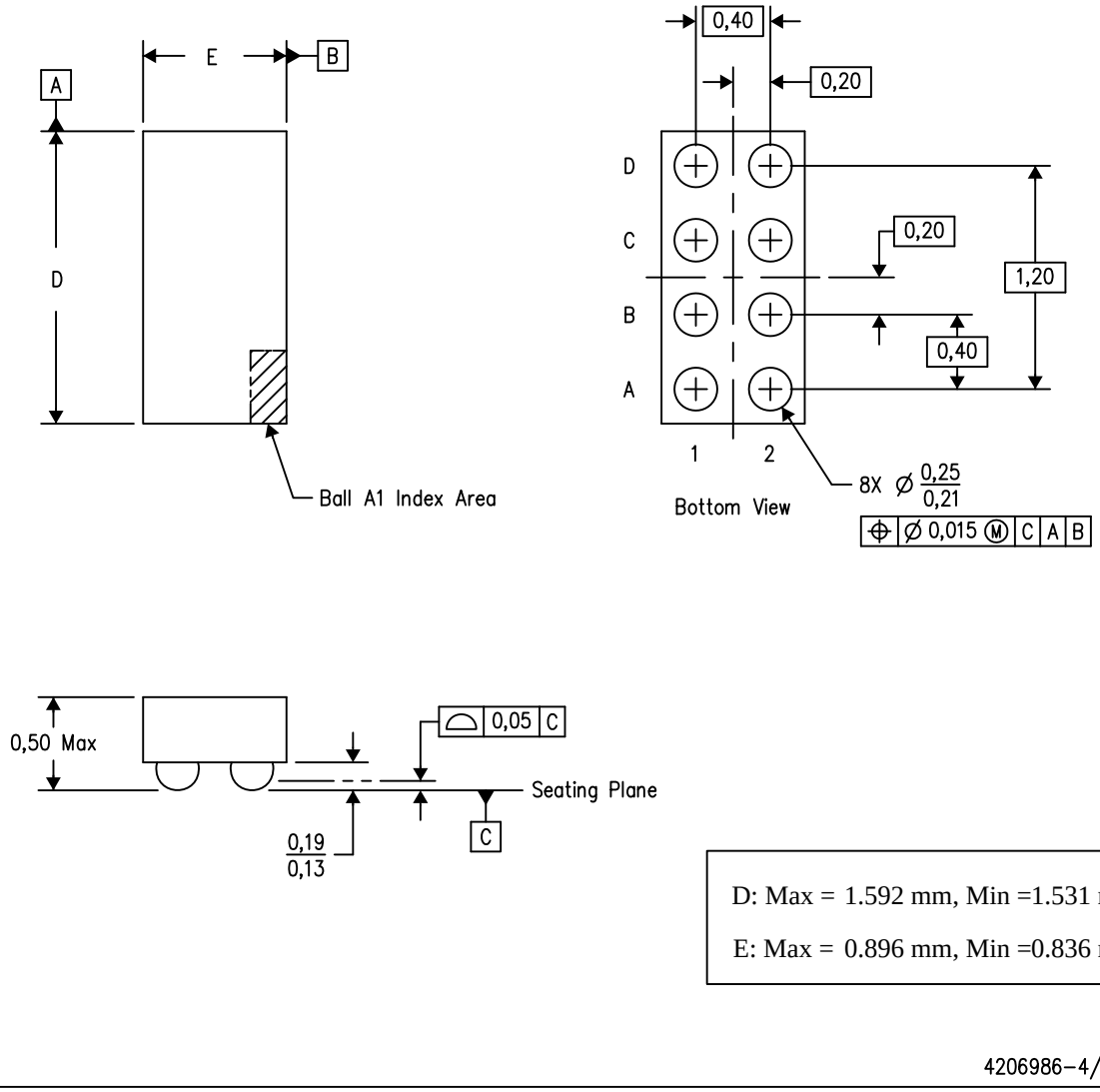
(5) Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "~" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.

(6) Lead/Ball Finish - Orderable Devices may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead/Ball Finish values may wrap to two lines if the finish value exceeds the maximum column width.

Important Information and Disclaimer: The information provided on this page represents TI's knowledge and belief as of the date that it is provided. TI bases its knowledge and belief on information provided by third parties, and makes no representation or warranty as to the accuracy of such information. Efforts are underway to better integrate information from third parties. TI has taken and continues to take reasonable steps to provide representative and accurate information but may not have conducted destructive testing or chemical analysis on incoming materials and chemicals. TI and TI suppliers consider certain information to be proprietary, and thus CAS numbers and other limited information may not be available for release.

In no event shall TI's liability arising out of such information exceed the total purchase price of the TI part(s) at issue in this document sold by TI to Customer on an annual basis.

YFP (R–XBGA–N8) DIE–SIZE BALL GRID ARRAY



NOTES:
A. All linear dimensions are in millimeters. Dimensioning and tolerancing per ASME Y14.5M–1994.
B. This drawing is subject to change without notice.
C. NanoFree™ package configuration.

NanoFree is a trademark of Texas Instruments

重要声明和免责声明

TI 均以“原样”提供技术性及可靠性数据（包括数据表）、设计资源（包括参考设计）、应用或其他设计建议、网络工具、安全信息和其他资源，不保证其中不含任何瑕疵，且不做任何明示或暗示的担保，包括但不限于对适销性、适合某特定用途或不侵犯任何第三方知识产权的暗示担保。

所述资源可供专业开发人员应用TI 产品进行设计使用。您将对以下行为独自承担全部责任：(1) 针对您的应用选择合适的TI 产品；(2) 设计、验证并测试您的应用；(3) 确保您的应用满足相应标准以及任何其他安全、安保或其他要求。所述资源如有变更，恕不另行通知。TI 对您使用所述资源的授权仅限于开发资源所涉及TI 产品的相关应用。除此之外不得复制或展示所述资源，也不提供其它TI或任何第三方的知识产权授权许可。如因使用所述资源而产生任何索赔、赔偿、成本、损失及债务等，TI对此概不负责，并且您须赔偿由此对TI 及其代表造成的损害。

TI 所提供产品均受TI 的销售条款 (http://www.ti.com.cn/zh-cn/legal/termsofsale.html) 以及ti.com.cn上或随附TI产品提供的其他可适用条款的约束。TI提供所述资源并不扩展或以其他方式更改TI 针对TI 产品所发布的可适用的担保范围或担保免责声明。

邮寄地址：上海市浦东新区世纪大道 1568 号中建大厦 32 楼，邮政编码：200122
Copyright © 2019 德州仪器半导体技术（上海）有限公司