

DESCRIPTION:

PFC MODEL SFB750-250-2-SS OPTION MECTRON 1 IS AN TWO CHANNEL SWITCHED FILTER BANK WITH INTEGRAL TTL DRIVER.

SPECIFICATIONS:

- FREQUENCY RANGE: CHANNEL 1: 750MHz ± 25MHz
CHANNEL 2: 750MHz ± 125MHz
- INSERTION LOSS: -4 dB Design goal -5 dB Max
- ISOLATION: 60 dB MIN.
- VSWR: 1.8:1 MAX.
- IMPEDANCE: 50 OHMS
- SWITCHING SPEED: 100 ns MAX. DELAY ON (50% TTL TO 90% RF)
100 ns MAX. DELAY OFF (50% TTL TO 10% RF)
- CONTROL: TTL COMPATIBLE UNIT LOAD TWO INDIVIDUAL CONTROLS
LOGIC "0" INSERTION LOSS
LOGIC "1" ISOLATION
- RF POWER RATING: +30 dBm CW MAXIMUM
- POWER SUPPLY: +5V @ 150 mA MAXIMUM
-12V to -15V @150 mA MAXIMUM
- CONNECTOR: SMA FEMALE (REMOVABLE)
- SIZE: 3.50" (W) X 3" (L) X 0.50" (H)
- WEIGHT: 6 OUNCES TYPICAL

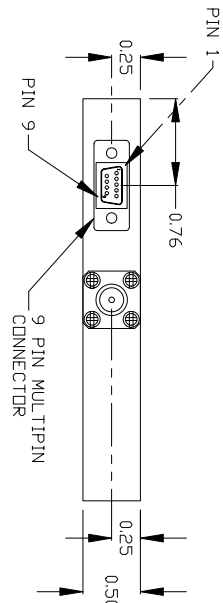
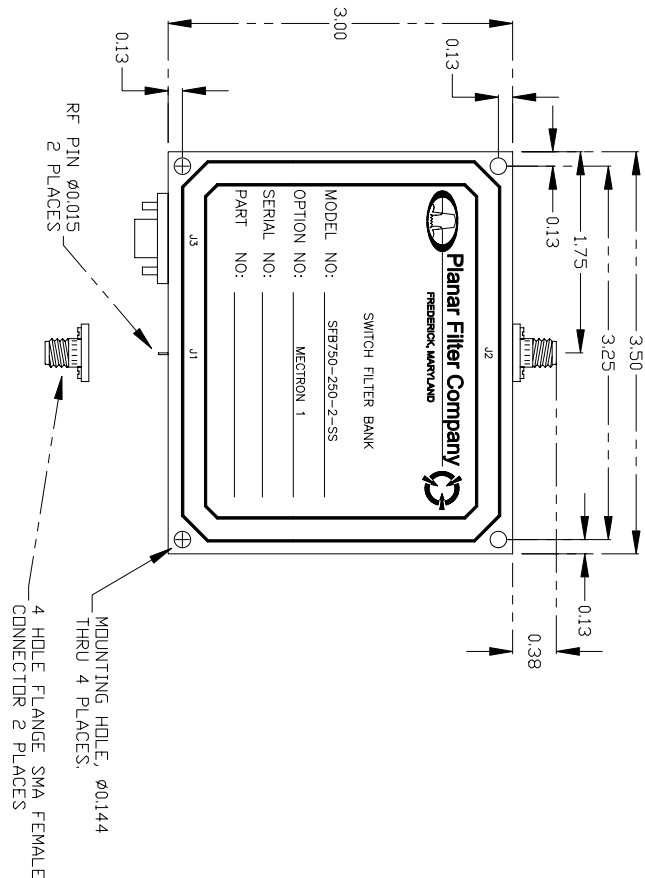
FILTER CHARACTERISTICS			
PARAMETER	FL1	FL2	UNITS
CENTER FREQUENCY	750	750	MHZ
1 dB LOW	MAX	725	MHZ
1 dB HIGH	MIN.	775	MHZ
60 dB LOW DESIGN GOAL	MIN.	DC TO 600	MHZ
60 dB DESIGN GOAL	MAX	900 MHz TO 20GHz	MHZ

ENVIRONMENTAL RATINGS:

- TEMPERATURE: -40°C TO +70°C (OPERATING)
-65°C TO +125°C (STORAGE)
 - HUMIDITY: MIL-STD-202F, METHOD 103B COND. B
 - SHOCK: MIL-STD-202F, METHOD 213B COND. B
 - VIBRATION: MIL-STD-202F, METHOD 204D COND. B
 - ALTITUDE: MIL-STD-202F, METHOD 105C COND. B
 - TEMPERATURE CYCLE: MIL-STD-202F, METHOD 107D COND. A
- NOTE: THE ABOVE SPECIFICATIONS ARE SUBJECT TO CHANGE OR REVISION

REVISIONS

ZONE	REV.	DESCRIPTION	DATE	APPROVED
-	-	ORIGINAL RELEASE	12/5/99	



LOGIC TABLE			
E1 = 0	J1-J2	750MHz ± 25MHz	
E1 = 1	J1-J2	750MHz ± 125MHz	

PIN DUT TABLE J1	
PIN	
1	+5V TO
2	GND
3	-12V/-15V
4	E1
5	N/C
6	N/C
7	N/C
8	N/C
9	GND

N/C = NO CONNECTION

ALL DIMENSIONS ARE IN INCHES
TOLERANCES:
X.XX ±0.010
X.XXX ±0.005

PART NO.		APPROVALS		DATE
		DRAWN		12/5/99
		CHECKED		
		ISSUED		
SIZE		FSCM NO.		DWG NO.
A		OZXZ8		155-0036
SCALE				SHEET 1 of 1

Planar Filter Company
FREDERICK, MARYLAND

OUTLINE DRAWING
SFB750-250-2-SS
OPTION MECTRON 1