CMD278

8-12 GHz GaN Low Noise Amplifier

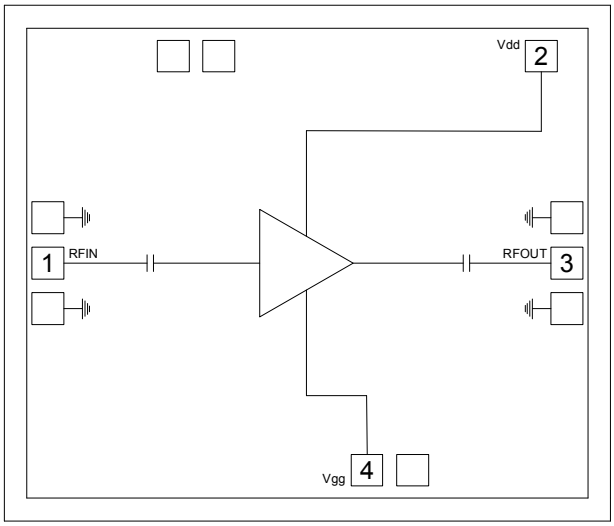
Features

- ► High gain
- ► Low noise figure
- ► High linearity
- ► High RF power survivability
- ► Small die size

Description

The CMD278 is a broadband MMIC GaN low noise amplifier die ideally suited for microwave radios and X-band applications where small size and high input power survivability are needed. The broadband device delivers 15 dB of gain with a corresponding output 1 dB compression point of +28 dBm and a noise figure of 1.8 dB. The CMD278 features an RF input power survivability of greater than 5 Watts.

Functional Block Diagram



Electrical Performance – V_{dd} = 10 V, I_{dd} = 280 mA, T_A = 25 °C, F = 10 GHz

Parameter	Min	Typ	Max	Units
Frequency Range		8 – 12		GHz
Gain		15		dB
Noise Figure		1.8		dB
Output P1dB		28		dBm
Output Psat		30		dBm
Output IP3		33		dBm
Input Return Loss		-19		dB
Output Return Loss		-15		dB
Supply Current		280		mA

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Specifications

Absolute Maximum Ratings

Parameter	Rating
Drain Voltage, Vdd	35 V
RF Input Power (CW or pulsed)	5 W
Channel Temperature, Tch	200 °C
Power Dissipation, Pdiss	9.27 W
Thermal Resistance, Θ_{jc}	12.4 °C/W
Operating Temperature	-55 to 85 °C
Storage Temperature	-55 to 150 °C

Exceeding any one or combination of the maximum ratings may cause permanent damage to the device.

Recommended Operating Conditions

Parameter	Min	Typ	Max	Units
Vdd *	5	10	28	V
Idd		280		mA
Vgg	-4	-1.85	-0.7	V

Electrical performance is measured at specific test conditions. Electrical specifications are not guaranteed over all recommended operating conditions.
* Derate Idd 10 mA/V for Vdd > 18.5 V

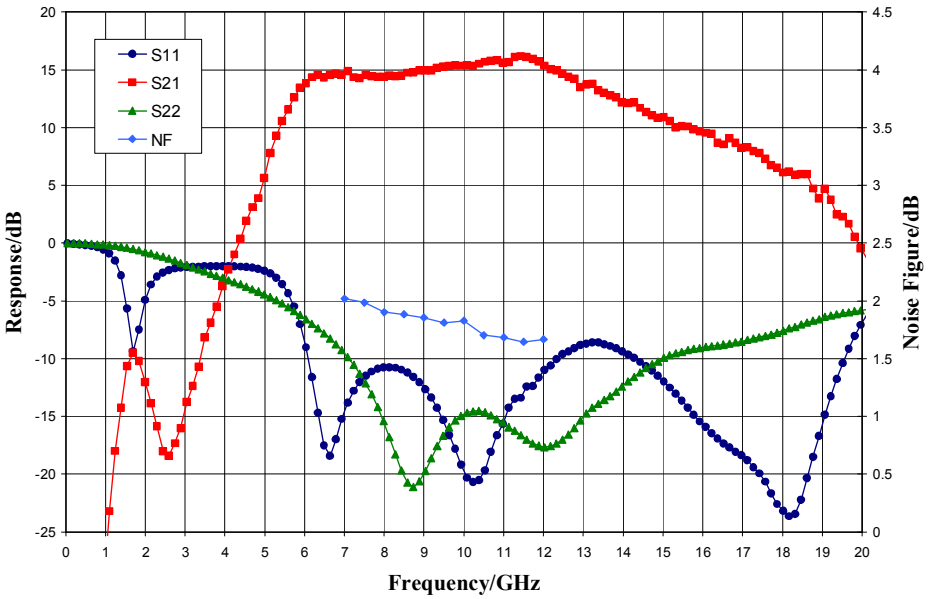
Electrical Specifications – V_{dd} = 10 V, I_{dd} = 280 mA, T_A = 25 °C

Parameter	Min	Typ	Max	Units
Frequency Range		8 – 12		GHz
Gain	12	15	19	dB
Noise Figure		1.8	2.5	dB
Input Return Loss		-19		dB
Output Return Loss		-15		dB
Output P1dB		28		dBm
Output IP3		33		dBm
Supply Current	200	280	360	mA
Gain Temperature Coefficient		0.02		dB/°C
Noise Figure Temperature Coefficient		0.01		dB/°C

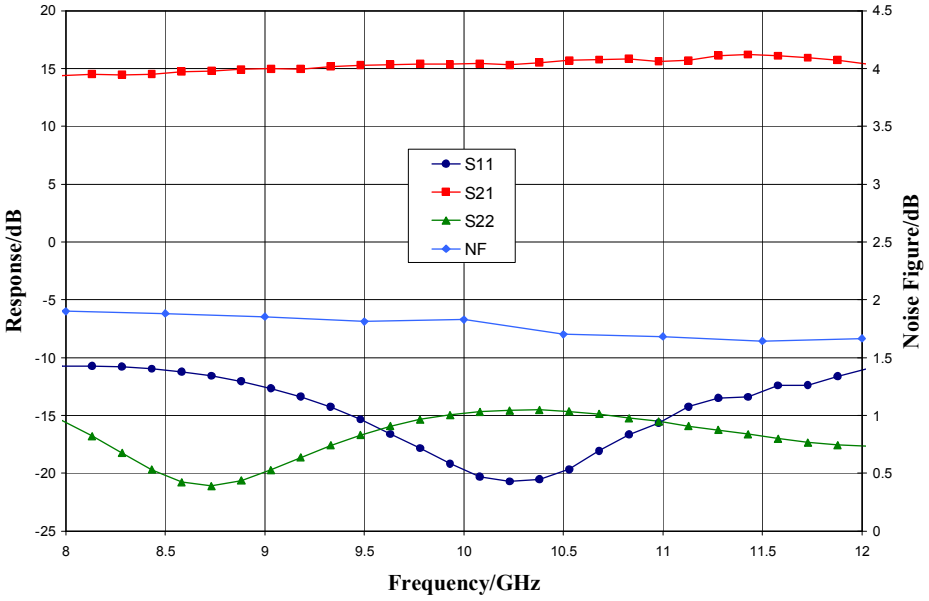
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Typical Performance

Broadband Performance, V_{dd} = 10 V, I_{dd} = 280 mA, T_A = 25 °C



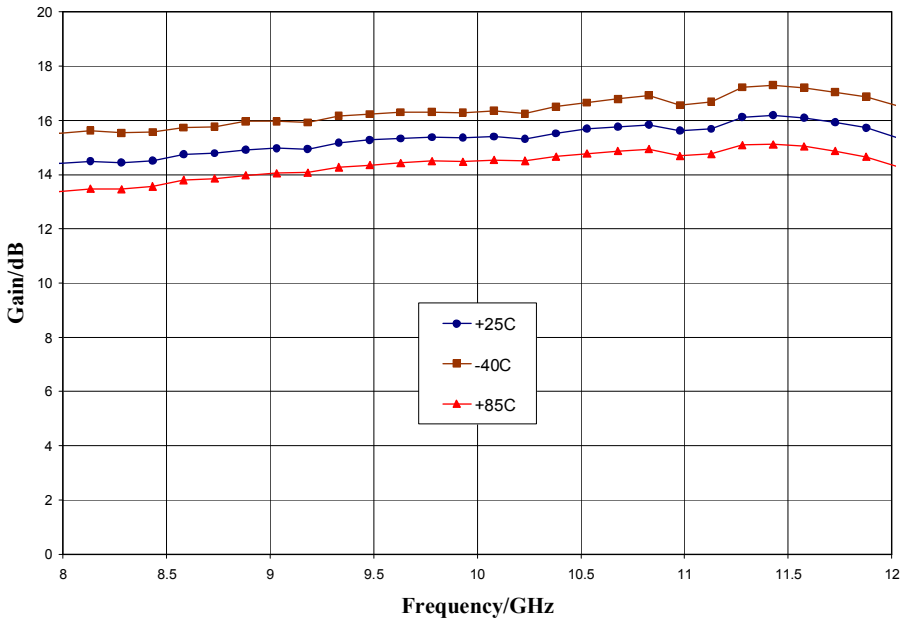
Narrow-band Performance, V_{dd} = 10 V, I_{dd} = 280 mA, T_A = 25 °C



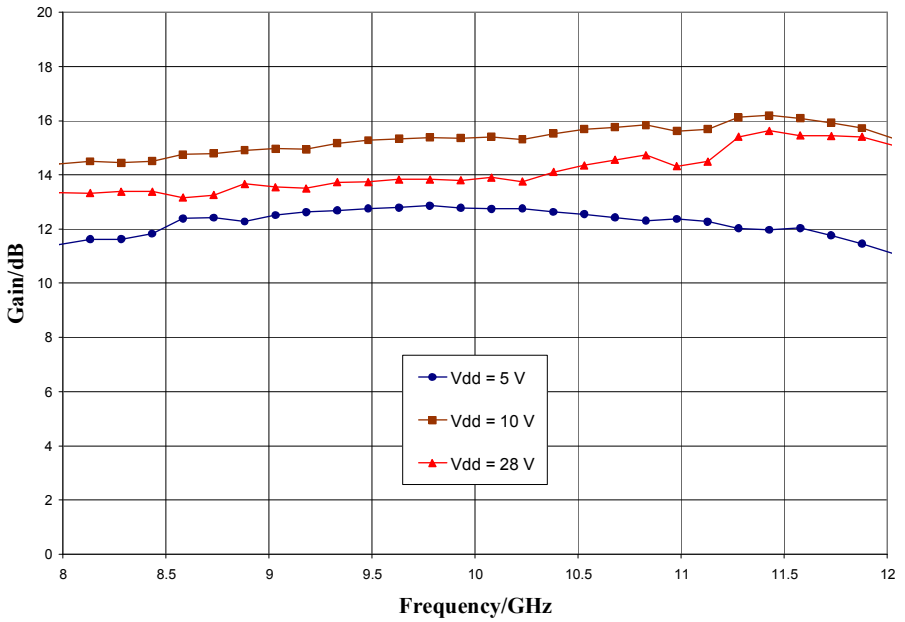
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Typical Performance

Gain vs. Temperature, V_{dd} = 10 V



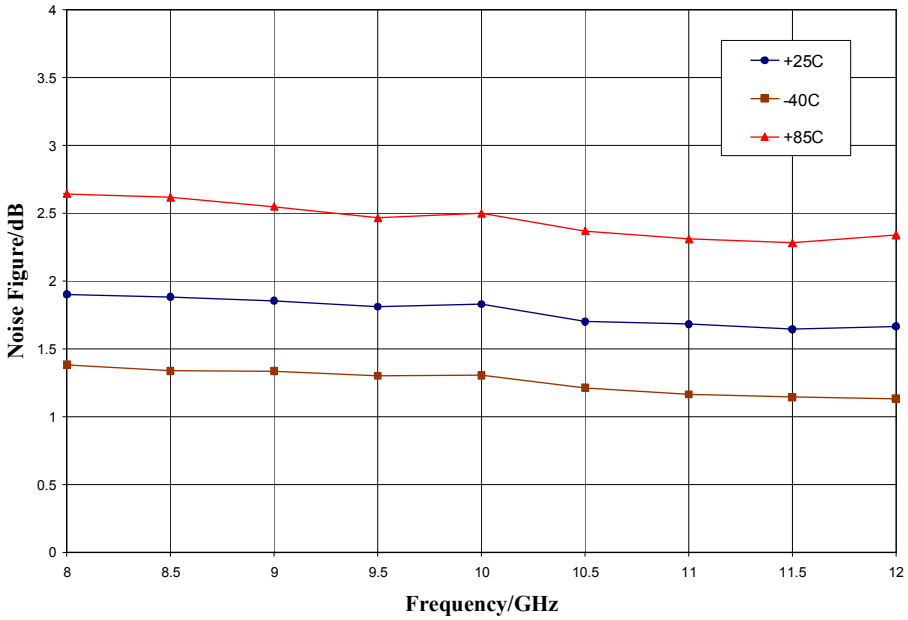
Gain vs. V_{dd}, I_{dd} = 280 mA, T_A = 25 °C



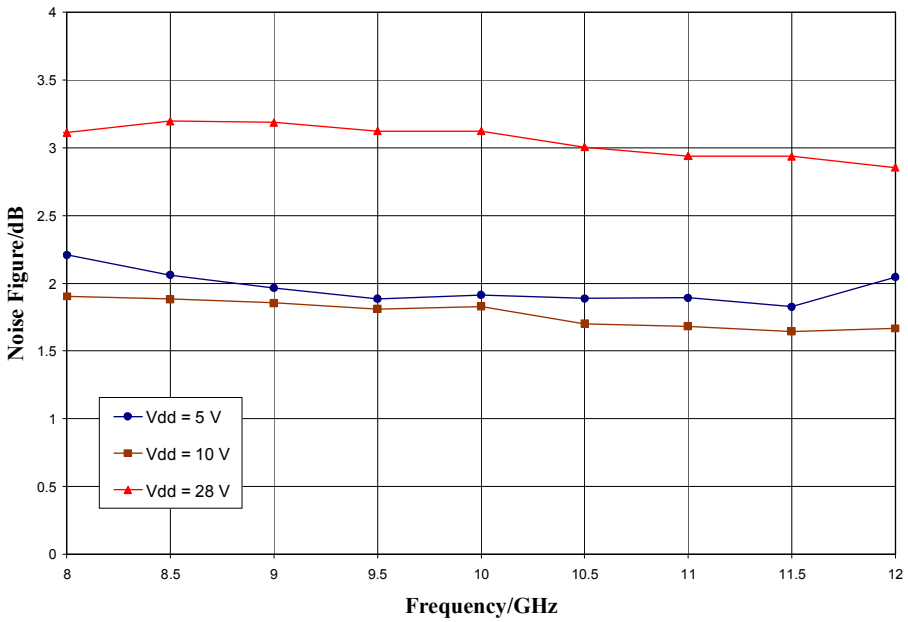
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Typical Performance

Noise Figure vs. Temperature, V_{dd} = 10 V



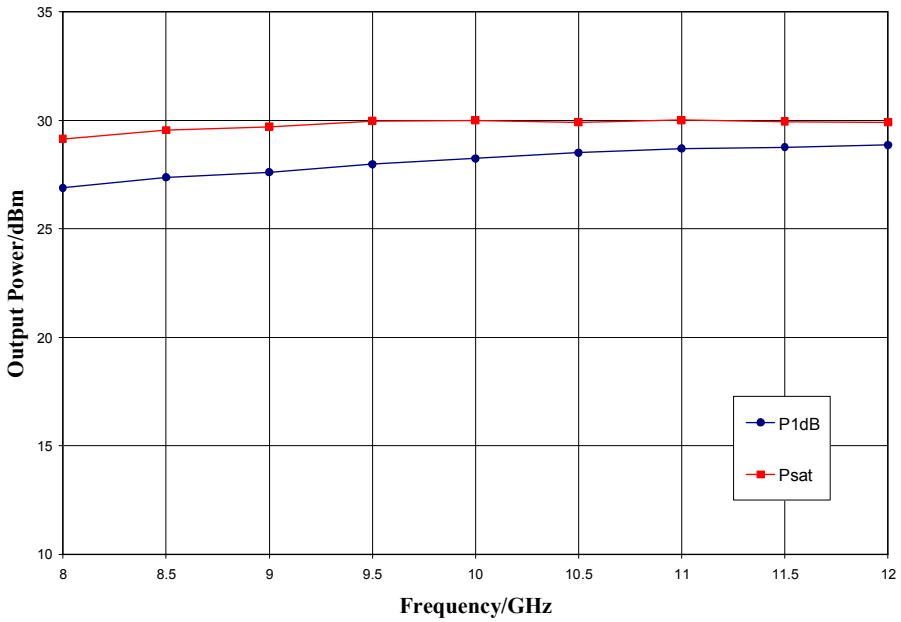
Noise Figure vs. V_{dd}, I_{dd} = 280 mA, T_A = 25 °C



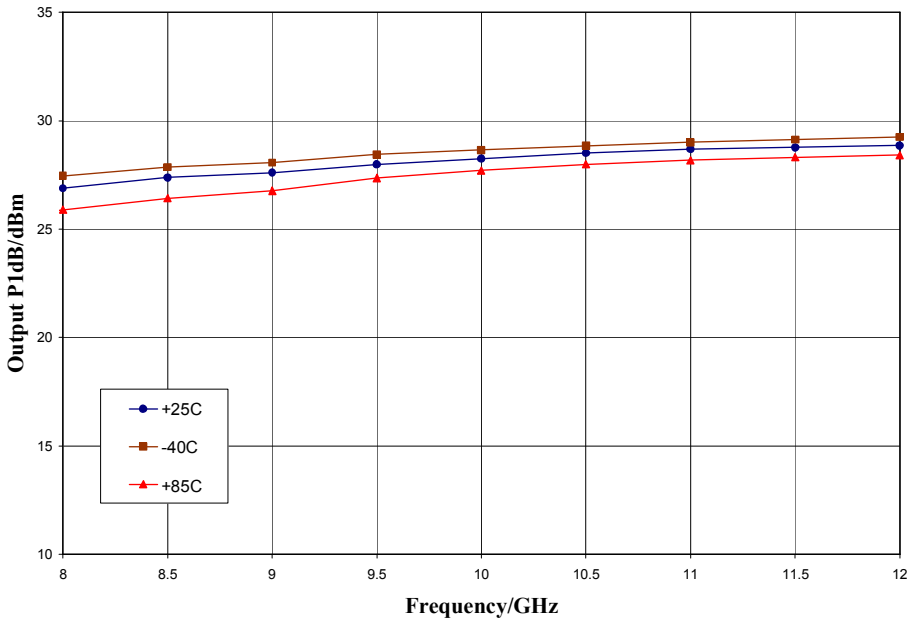
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Typical Performance

Output Power, V_{dd} = 10 V, I_{dd} = 280 mA, T_A = 25 °C



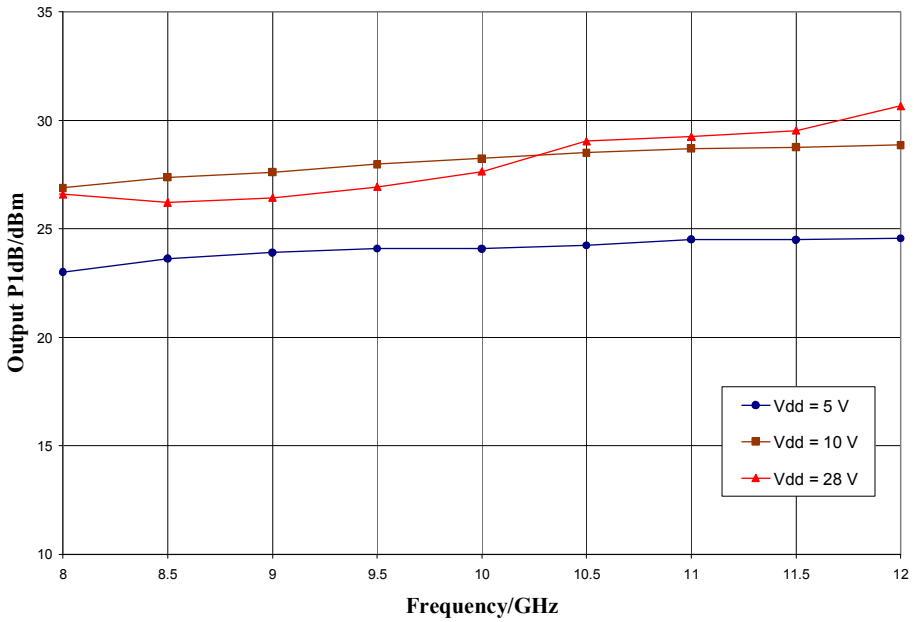
P1dB vs. Temperature, V_{dd} = 10 V



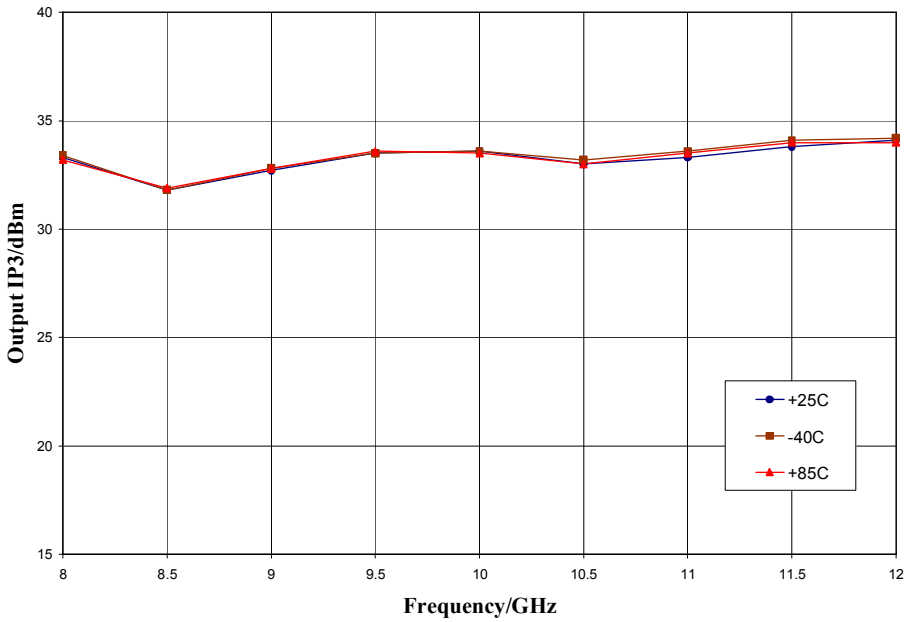
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Typical Performance

P1dB vs. V_{dd}, I_{dd} = 280 mA, T_A = 25 °C



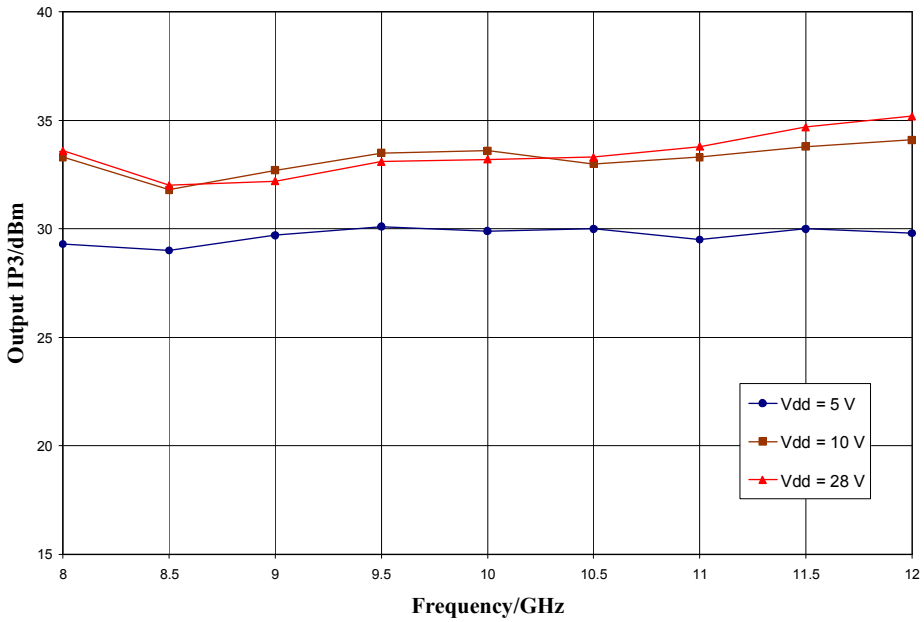
Output IP3 vs. Temperature, V_{dd} = 10 V



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Typical Performance

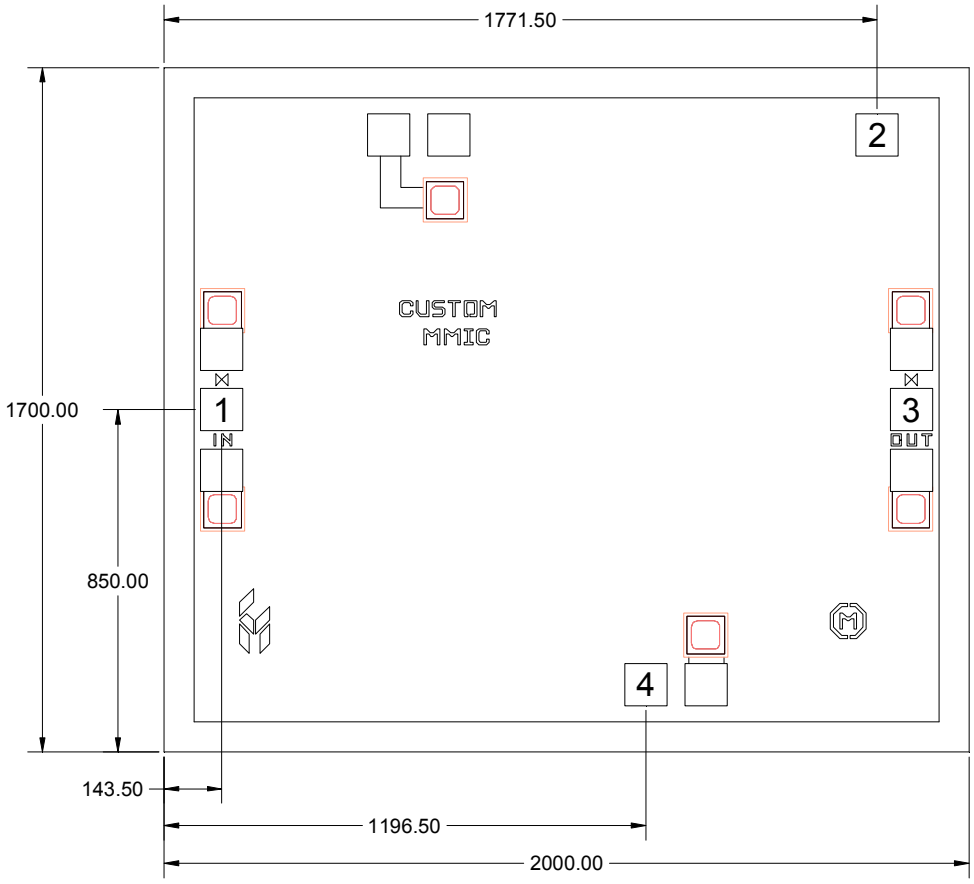
Output IP3 vs. V_{dd}, I_{dd} = 280 mA, T_A = 25 °C



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Mechanical Information

Die Outline (all dimensions in microns)



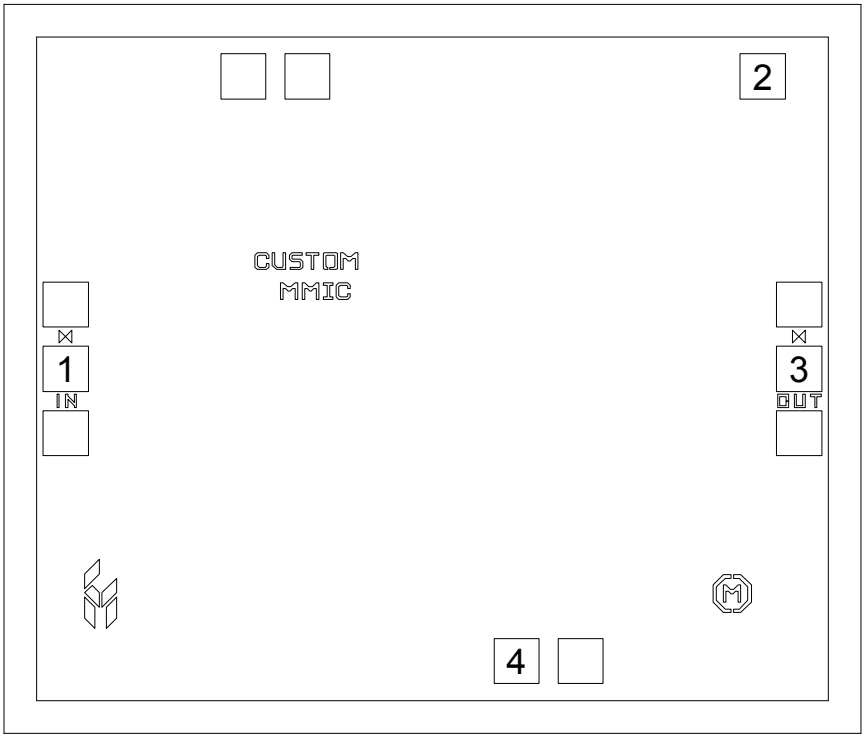
Notes:

1. No connection required for unlabeled pads
2. Backside is RF and DC ground
3. Backside and bond pad metal: Gold
4. Die is 100 microns thick
5. DC bond pads (2, 4) are 100 x 100 microns
6. RF bond pads (1, 3) are 100 x 100 microns

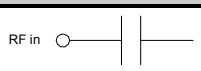
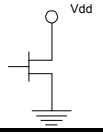
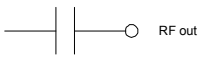
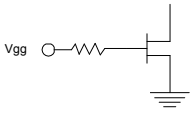
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Pad Description

Pad Diagram



Functional Description

Pad	Function	Description	Schematic
1	RF in	DC blocked and 50 ohm matched	
2	Vdd	Power supply voltage Decoupling and bypass caps required	
3	RF out	DC blocked and 50 ohm matched	
4	Vgg	Power supply voltage Decoupling and bypass caps required	
Backside	Ground	Connect to RF / DC ground	

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Applications Information

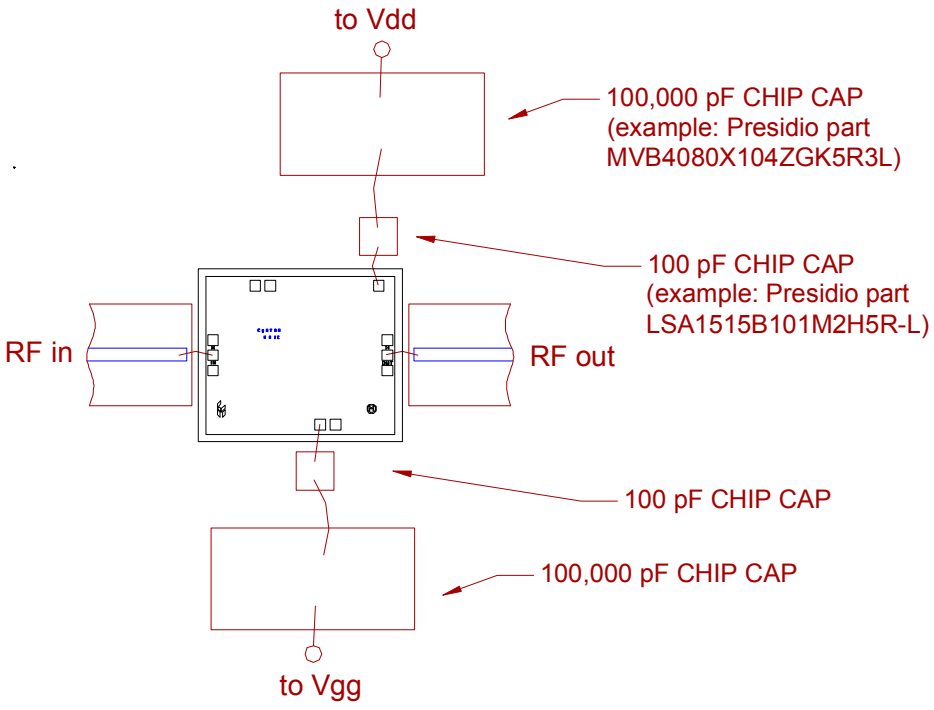
Assembly Guidelines

The backside of the CMD278 is RF ground. Die attach should be accomplished with electrically and thermally conductive epoxy or eutectic attach. Standard assembly procedures should be followed for high frequency devices. The top surface of the semiconductor should be made planar to the adjacent RF transmission lines, and the RF decoupling capacitors placed in close proximity to the DC connections on chip.

RF connections should be made as short as possible to reduce the inductive effect of the bond wire. Use of a 0.8 mil thermosonic wedge bonding is highly recommended as the loop height will be minimized. The RF input and output require a single bond wire as shown.

The semiconductor is 100 um thick and should be handled by the sides of the die or with a custom collet. Do not make contact directly with the die surface as this will damage the monolithic circuitry. Handle with care.

Assembly Diagram



GaN MMIC devices are susceptible to damage from Electrostatic Discharge. Proper precautions should be observed during handling, assembly and test.

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Applications Information

Biasing and Operation

The CMD278 is biased with a positive drain supply and a negative gate supply. Performance is optimized when the drain voltage is set to +10 V but may be set between +5 V and +28 V.

Turn ON procedure:

1. Apply gate voltage V_{gg} and set to a voltage sufficient to pinch off drain current (~ -4 V)
2. Apply drain voltage V_{dd} and set to +10 V
3. Increase Vgg (less negative) to achieve a drain current of 280 mA

Turn OFF procedure:

1. Turn off drain voltage V_{dd}
2. Turn off gate voltage V_{gg}

RF power can be applied at any time.

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