

# **LEAD-FREE / RoHS-COMPLIANT**

#### **HIGH POWER SURFACE-MOUNT BALUN**

#### **BALH-0006SMG**

#### **Features**

- 500 kHz to 6 GHz Balun (Balanced to Unbalanced Transformer)
- High 37 dBm 1-dB compression enables high power applications
- Tuned for Optimal Phase/Amplitude Balance
- Applications: Balanced Amplifiers, Baseband Digital Modulation, Signal Integrity
- BALH-0006SMG.s3p

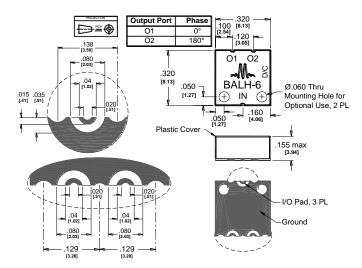


**Electrical Specifications -** Specifications guaranteed from -55 to +100°C, measured in a  $50\Omega$  system.

Parameter	Frequency Range	Min	Тур	Max
Insertion Loss (dB)			5	6.5
Input 1 dB Compression (dBm)			37	
Nominal Phase Shift (Degrees)			180	
Amplitude Balance (dB)			±0.2	±1.2
Phase Balance (Degrees)	500 kHz to 6 GHz		±3	±10
Common Mode Rejection (dB)		17	30	
Isolation (dB)			6	
VSWR (Input) VSWR (Output)			2.1 1.2	
Risetime /Falltime (ps) <sup>1</sup>			22	

<sup>&</sup>lt;sup>1</sup>Specified as 90%/10%. Calculated from  $\tau_{balun}^2 = (\tau_{out}^2 - \tau_{in}^2)$ 

Model Number	Description	
BALH-0006SMG	500 kHz to 6 GHz Balun, High Power, Surface Mount,	
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Substrate material is 8-mil thick Rogers 4003, 1 Oz Electrodeposited Cu. I/O Pads & Ground Plane Finish is Gold Flash, 5 to 10 µ-inches, over Electroplated Nickel, 100-200 µ-inches, over Cu. See BALSMG-PCB for suggested PCB layout.

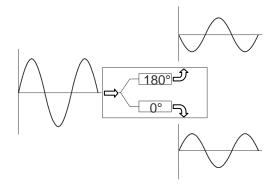


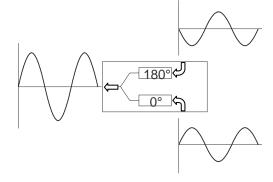
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# **Block Diagram**

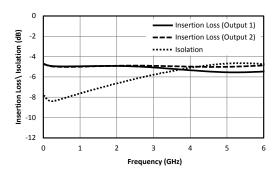




Single ended to differential

Differential to single ended

### **Typical Performance**



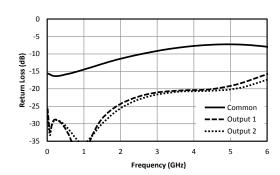
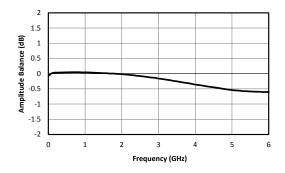


Fig. 1. Common to output port insertion loss and output to output port Isolation.

Fig. 2. Return loss for common port and output ports.



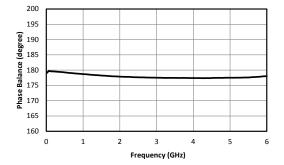


Fig. 3. Amplitude balance between output ports.

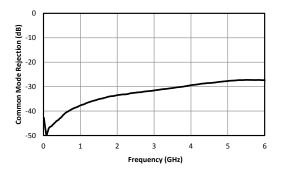
Fig. 4. Phase balance between output ports.



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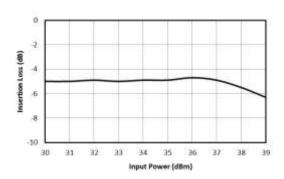
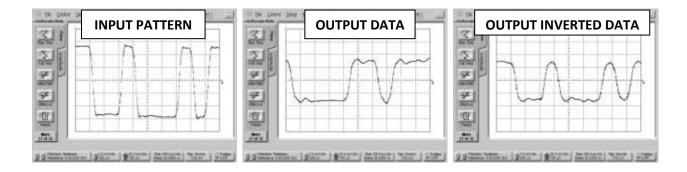


Fig. 5. Common mode rejection.

Fig, 6. Output Compression



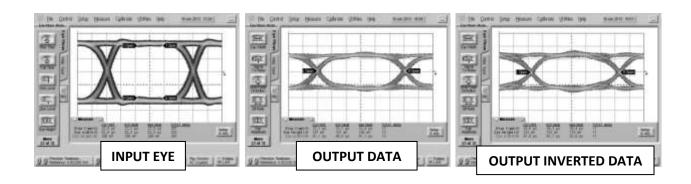


Fig. 6. Oscilloscope measurements of the BALH-0006SMG with a 10 Gb/s PRBS pattern. Bit pattern is measured with a  $2^7$ -1 PRBS input demonstrating extremely good pulse fidelity for both inverted and non-inverted output. Eye diagrams are taken with a  $2^{31}$ -1 PRBS input demonstrating minimal eye distortion/closure afforded by the extremely low frequency operation of the balun (<500 kHz).



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# **DC** Interface

Port	Description	DC Interface Schematic	
Common Port / In (Unbalanced)	The common port is DC short to ground.	Common Port +	
Out 1 / 0° Port (Balanced)	The 0° port is DC short to ground.	O° Port (Balanced)	
Out 2 / 180° Port (Balanced)	The 180° port is DC short to ground.	180° Port (Balanced)	