

**EQ6-40** 

#### **Features**

- 40 Gb/s Equalizer
- Matched 50 Ohm Impedance on Input and Output Ports
- Applications: Digital Communications, Signal Integrity
- EQ6-40.s2p



### **Electrical Specifications -** Specifications guaranteed from -55 to +100 $^{\circ}$ C, measured in a 50 $\Omega$ system.

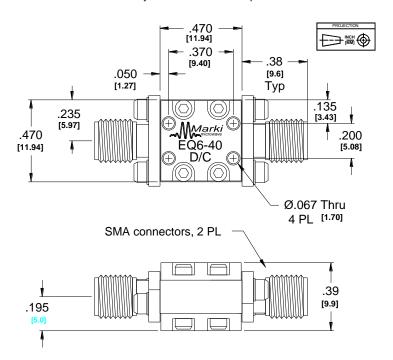
Parameter	Operating Frequency	Min	Тур	Max
Insertion Loss (dB)	10 MHz		6	
	15 GHz		2	
	40 GHz		1	
VSWR (Input)	DC – 40 GHz		1.3	
VSWR (Output)			1.3	
Risetime /Falltime (ps) <sup>1</sup>			11	
Total Input Power (W)				0.25
Weight (g)			9	

<sup>&</sup>lt;sup>1</sup>Specified as 20%/80%.

Amplitude- and phase-matched pairs available; contact factory.

Model Number	Description
EQ6-40	40 Gb/s Equalizer with 2.92 mm connectors <sup>1</sup>

<sup>&</sup>lt;sup>1</sup>Default is 2.92 mm female connectors. Consult factory for other connector options.





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### **Typical Performance**

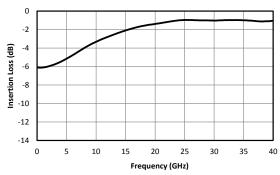


Figure 1: Insertion loss

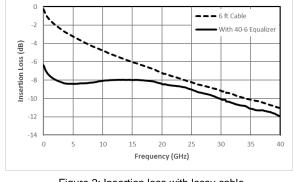


Figure 2: Insertion loss with lossy cable

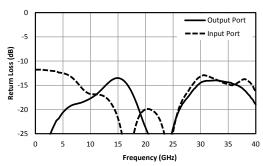


Figure 3: Return loss

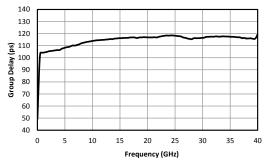


Figure 4: Group delay



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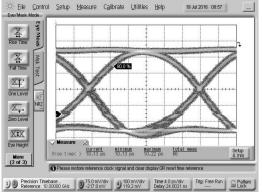


Figure 5: Input eye

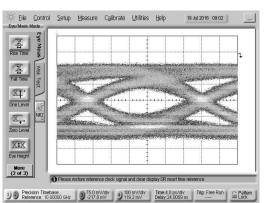


Figure 7: Eye after lossy cable

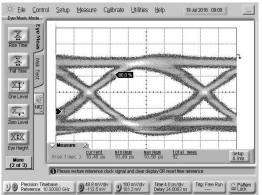


Figure 9: Eye after equalization

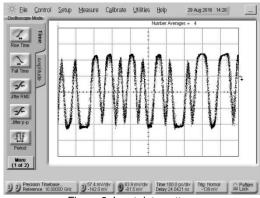


Figure 6: Input data pattern

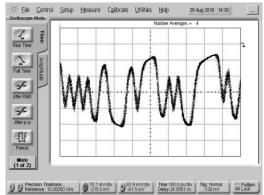


Figure 8: Data pattern after lossy cable

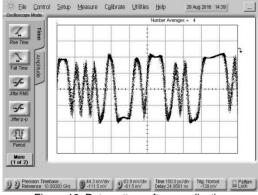


Figure 10: Data pattern after equalization

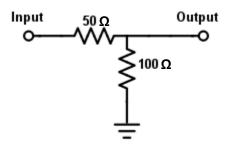
Oscilloscope measurements of the EQ6-40 with a 40 Gb/s PRBS pattern. Bit pattern is measured with a  $2^{7}$ -1 PRBS input. Eye diagrams are taken with a  $2^{31}$ -1 PRBS input. Figure 6, "Input data pattern", is uncorrelated with the other patterns due to the additional cable length.



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### **DC Schematic**



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